

Operations Notes

FOS Team @ ESAC

Reported by:

J. Fauste/J.M. Castro Cerón

Topic: Date:

Issue:

FOS Report for week 45, year 2015

from 02 NOV 2015 to 09 NOV 2015

1.0

1 General Comments

Activities scheduled for this week are those planned for the 45th calendar week of 2015:

02 NOV 2015 to 09 NOV 2015 (DoYs 306 to 313).

The following routine activities were planned this week (see Gantt chart on next page and CRF 534):

- One PMS Offset on 05 NOV 2015 (DoY 309), including three Short Calibrations at 14:22:00.0z, 14:22:34.8z, and 14:23:09.6z (orbit 31577).
- Local Oscillator Calibrations every 10 minutes.

X band Passes over ESAC and Svalbard.

2 Mission Planning Deviations

None.

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| | NOV-2015 | | | | | | |
|-------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|
| | Mon, 02-NOV-2015 | Tue, 03-NOV-2015 | Wed, 04-NOV-2015 | Thu, 05-NOV-2015 | Fri, 06-NOV-2015 | Sat, 07-NOV-2015 | Sun, 08-NOV-2015 |
| ⊟- ∕ SMOS | | | | | | | |
| - 🚪 ESAC_PASS | | | | | | | |
| 🚪 KSAT_PASS | | | | | | | |
| elocal_oscilator | | | | | | | |
| PMS_OFFSET | | | | | | | |
| BAND_PASS | | | | | | | |
| | | | | | | | |
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3 TC Failures

None.

4 Unforeseen Out of Limits (OOLs)

• Two unforeseen out of limits were received at the time of the Mass Memory latch up on 2015-11-06T15:14:04z (see Appendix-A for further details)

Topic:

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5 On Board Anomalies

• A new Mass Memory Latch up in partition P4 happened on 2015-11-06T15:14:04.751z. This event was geolocated over the South Indian Ocean at the following coordinates:

Longitude= 32.378963 Latitude= :-46.986863

No data losses happened since both Read and Wrote pointers were located in partition P0 at the time of the anomaly. Recovery took place on 2015-11-09T23:00:00 via CRF-536

6 On Board Events Telemetry

The following RAM Single Bit Errors befell this week:

| Event Description | Severity | Event Time | Parameters |
|--------------------------|----------|-----------------------|------------|
| RAM single Bit Error | WARN | 2015.306.09.56.48.384 | 21135CC |

7 FOS Systems Status

 The PLPC Time Correlation task crashed on 2015-11-04T19:55:09z (Earth Reception Time) at that time the following message appeared on SCOS Event Logger:

"CORBA SystemException caught"

As result of this failure the Time Correlation task crashed at 19:55:01z (ERT). Since the problem appeared during the evening and outside nominal working hours, the system remained in that wrong configuration until the following morning when the TCO task was again manually restarted around 2015-11-05T08:24:27 (ERT). As result of this problem wrong telemetry data was also archived on the SMTA MUST machine. The feeding data *cronjobs* on SMTA were made disabled and the TM data for the following SBand passes deleted from PLPC system:

AUS-11 19:20 -- 19:33 AUS-12 04:55 -- 05:10



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AUS-13 06:35 -- 06:48 KUX-5 09:44 -- 09:58

After the TM deletion on the PLPC HFA files, the affected passes were again ingested into the system and the following TM files processed again on PLPC system:

SMO_PLTM1_P_2015_11_05_09_46_59 SMO_PLTM1_P_2015_11_05_06_38_59 SMO_PLTM1_P_2015_11_05_05_02_23 SMO_PLTM1_P_2015_11_05_05_00_11 SMO_PLTM1_P_2015_11_05_04_58_00 SMO_PLTM1_P_2015_11_04_19_27_00 SMO_PLTM1_P_2015_11_04_19_24_49

TM data wrongly archived on the SMTA machine was also deleted and again reingested from PLPC.

8 Data Reception from CNES

All S Band Passes were correctly received from CNES and successfully processed by the FOS PLPC System.

9 X Band Data Reception in PXMF

None, all S Band Passes successfully received and processed.

10 Exceptional Activities

None.

11 AOB

None.





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APPENDIX A: OOLs

The following two MIRAS Out of Limtis were received on the FOS PLPC system at the time of the Mass Memory Latch up on partition P4.

Topic:

Date:

| GS_TIME | OBTIME | PARAMETER | DESCRIPTION | OOL Value | Check Value |
|-----------------------|-----------------------|-----------|-----------------|-----------|-------------|
| 2015.310.18.51.21.713 | 2015.310.15.14.04.751 | DMASME37 | SDD LU Detected | FALSE | TRUE |
| 2015.310.18.51.21.713 | 2015.310.15.14.04.751 | DMASME08 | LU Switch P4 | OFF | ON |