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Abstract : This document provides a summary of the status and performance of SMOS over the

course of the reporting month

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AMENDMENT POLICY

This document shall be amended by releasing a new edition of the document in its entirety. The Amendment Record Sheet below records the history and issue status of this document.

AMENDMENT RECORD SHEET

ISSUE	DATE	DCI No	REASON	
1.0	14/09/2021	N/A	Formal release	

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1 EXECUTIVE SUMMARY

This is the routine Soil Moisture and Ocean Salinity (**SMOS**) Monthly Public Report containing a summary of the instrument health, product quality status and updates to SMOS processing and auxiliary files during August 2021.

The instrument health during August 2021 was found to be nominal. There were 4 unavailabilities reported during the reporting period which translated into time intervals with data loss or degraded data. The list of unavailabilities is included in section 3.2.

The data quality during August 2021 was found to be nominal, with the exceptions listed in section 4.5. These degraded periods have been induced either by instrument anomalies or by unavailability of dynamic auxiliary files.

2 INTRODUCTION

2.1 Structure of the Document

After this introduction, the document is divided into a number of major sections that are briefly described below:

1 Executive summary

The executive summary covers the main findings from the report.

2 Introduction

A list of referenced documents and definitions of terms are available.

3 Instrument status

This section covers the instrument health and unavailabilities from this reporting period.

4 Data Summary

This section covers reprocessing, updates to processors and aux files as well as a data coverage summary.

5 Long Term Analysis

Long-term analysis of the instrument calibration and data quality are provided in this section.

2.2 Reference Documents

RD1 XSMS-GSEG-EOPG-TN-08-0016 SMOS L1OP-V3 Product Quality Flag format definition V3.1

2.3 **Definitions of Terms**

The following terms have been used in this report with the meanings shown.

Term	Definition
CCU	Correlator and Control unit, instrument computer on-board
CMN	Control and Monitoring Node, responsible for commanding the receivers, reading their physical temperatures and telemetry and the generation of the synchronization signal (local oscillator tone) among receivers.
DPGS	Data Processing Ground Segment
ESL	Expert Science Laboratory
IC4EC	Internal Calibration for External calibration. Calibration sequences for the instrument monitoring and calibration of science data acquired in external target pointing.

IFREMER French Research Institute for Sea Exploitation (Institut

Français de Recherche pour l'Exploitation de la MER)

IPF Instrument Processor Facility

L2OS Level 2 Ocean Salinity

L2SM Level 2 Soil Moisture

LICEF Lightweight Cost Effective Receivers

MIRAS Microwave Imaging Radiometer using Aperture Synthesis

MM Mass Memory

N/A Not applicable

OCM Orbit Correction Manoeuvre

Pi-MEP SMOS Pilot-Mission Exploitation Platform project

PMS Power Measurement System

RFI Radio Frequency Interference

SPQC Systematic Product Quality Control facility

SSS Sea Surface Salinity

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3 INSTRUMENT STATUS

3.1 Instrument health

The current instrument status is that all the **instrument** subsystems are working correctly. The current configuration of the instrument is that the arm A and the arm B are working in nominal side and arm C is in the redundant side.

Table 3-1 History of instrument problems and mode changes

Start	Stop	Description
11 January 2010 12:07z Orbit 1013	N/A	Arm A changes from redundant to nominal side. That operation is to avoid the malfunction of one of the redundant CMNs of the arm.
12 January 2011 09:15z Orbit 6278	N/A	Arm B changes from redundant to nominal side. That operation is to avoid the malfunction of one of the redundant CMNs of the arm.

3.2 Instrument unavailabilities and anomalies

The unavailabilities and anomalies listed in Table 3-2 occurred during the reporting period. A full list of unavailabilities can be found in the Mission Status section on the SMOS Earthnet website accessible here

During these unavailabilities and anomalies the instrument may have either not collected data or may have collected corrupt data which may not have been processed to higher levels. Table 4-7, Table 4-8 and Table 4-9 provide details of the data which has been affected by gaps and quality degradation respectively.

Table 3-2 SMOS unavailability list

Start Time (UTC)	Stop Time (UTC)	Unavailability Report Refer- ence	Planned	Description
02/08/2021 07:33:24	02/08/2021 07:53:26	FOS-4892	No	CMN Unlock
06/08/2021 20:29:35	06/08/2021 20:33:04	FOS-4916	No	MM Latch-Up Recovery
17/08/2021 09:31:37	17/08/2021 09:41:37	FOS-4915	No	CMN Unlock
30/08/2021 20:27:15	30/08/2021 20:37:15	FOS-4931	No	CMN Unlock

4 DATA SUMMARY

4.1 Reprocessing activities

The information regarding to data reprocessing activities (REPR data type) is shown in the table below.

Table 4-1 Data Summary - REPR

Data type	Sensing start	Sensing stop	Version	Comments	
L2	31/03/2020	31/01/2021	V700	L2 V700 Reprocessing Catch-Up Campaign started on March 8th, 2021 and finished on March 27th, 2021.	
L1	31/03/2020	31/01/2021	V724	L1 V724 Reprocessing Catch-Up Campaign began on March 8th, 2021 and finished on March 27th, 2021.	
L2	13/04/2010	31/03/2020	V700	L2 V700 Full Reprocessing Campaign started on December 5 th , 2020, and finished on March 4 th , 2021.	
L1	01/10/2019	31/03/2020	V724	L1 V724 Reprocessing Catch-Up Campaign began on May 22 nd , 2020 and finished on May 29 th , 2020.	
L1	01/01/2010	31/09/2019	V724	L1 V724 Reprocessing Campaign began on January 2 nd , 2020 finished on February 12 th , 2020. Reprocessing of pending products finished on February 19 th , 2020 concluding this reprocessing activity.	

4.2 Operational activities

The information regarding to the data regeneration activities (OPER data type) is shown in the table below:

Table 4-2 Data Summary - OPER

Reporting period					
Data type Sensing start Sensing stop		_	Comments		
		Previ	ous reporting periods		
Data type Sensing Sensing Comments start Stop					
L2 15/06/2021 17/06/2021 01:42:22z 05:27:55z			Reprocessed period due to incorrect LSMASK orchestrated in the DPGS.		
L1	15/06/2021 01:42:22z	17/06/2021 05:27:55z	Reprocessed period due to incorrect LSMASK orchestrated in the DPGS.		



The information regarding the past version V5xx data regeneration and reprocessing activities (OPER and REPR data type) are available in the monthly report of April 2015.

The information regarding the past version v6xx data regeneration and reprocessing activities (OPER and REPR data type) are available in the monthly report of May 2021.

4.3 Processing changes

4.3.1 Processor updates

During the reporting period, no new processor versions were deployed into operations.

4.3.2 Processor Status

At the end of the reporting period, the Processing Facility is using the following processors:

Table 4-3 Instrument Processors status

Processor	Version	Deployment date
L1OP	724	25/05/2021
L2OS	700	25/05/2021
L2SM	700	25/05/2021

Table 4-4 Pre- and Post-processors status

Processor	Version	Deployment date
ECMWFP	318	07/11/2013
VTECGN	320	18/05/2016
OSCOTT	700	25/05/2021
L2 Post-processors	600	25/05/2021
SNOWP	102	28/10/2016

4.3.3 Schema updates

No updates for product schema in the reporting period.

4.3.4 Schema status

At the end of the reporting period, the schema version of the datablock of the products generated and distributed through SMOS dissemination service is:

Table 4-5 Schema version status

Product type	Version
MIR_SC_F1B	402
MIR_SCSF1C	401
MIR_SCLF1C	401

Product type	Version
MIR_BWSF1C	400
MIR_BWLF1C	400
MIR_SMUDP2	400
MIR_OSUDP2	401
AUX_ECMWF_	300

The schema package v07.08.01, the XML Read/Write API libraries to read SMOS products, visualization and mapping tools for SMOS L1 and L2 products are available here (See GMT for the schemas)

Further information about the product format is available in the level 1 and level 2 product specification documents available here

4.3.5 Aux file updates

The following quasi-static and dynamic AUX files were disseminated to the processing stations this reporting period. The status of the quasi-static and dynamic AUX files at the end of the reporting period is in the section 7.

AUX_BULL_B

Note that, as reported in SPCM37, the format of the BULL_B primitive has been manually modified in order to obtain a properly formatted SMOS file. This is due to a small format change since February 2019 Bulletin B.

SM_OPER_AUX_BULL_B_20210602T000000_20210701T235959_120_001_3
Start sensing time at L1 processor: N/A

Justification: Bulletin Update including values from May 2021 and the prediction for June 2021. Its usage is intended for reprocessing.

Justification: Bulletin Update including values from May 2021 and the prediction for June 2021. Its usage is intended for the nominal production.

4.4 Calibration Events Summary

The following table summarizes the major calibration activities conducted during the reporting period. The Local Oscillator calibration is not included in the table since occurs periodically every 10 minutes. The short calibrations are acquired weekly since 2011-03-24 and they are currently used in the nominal processing chain.

Table 4-6 Calibration summary

Date	Start Time	Stop Time	Calibration	Comments
04/08/2021	04:44:23	06:06:36	Warm NIR	Nominal
05/08/2021	06:42:00	06:43:44	Short	Nominal
12/08/2021	07:26:20	07:28:04	Short	Nominal
18/08/2021	03:57:37	05:19:50	Warm NIR	Nominal



Date	Start Time	Stop Time	Calibration	Comments
19/08/2021	07:53:50	07:55:34	Short	Nominal
26/08/2021	13:10:00	15:43:19	Long	Nominal

4.5 **Data Coverage Summary**

Where instrument unavailabilities or anomalies have occurred during this reporting period, gaps in data coverage may have also occurred. A list of the gaps due to a permanent data loss is given in Table 4-7 by product level. On the other hand, a list of gaps due to operational problems is given in Table 4-8. The latter gaps may be recovered when the problem is fixed.

The science data gaps due to the execution of calibration activities are not listed in this section.

Table 4-7 Data loss summary

Start	Finish	Data Level	Comments
06/08/2021 20:29:35	06/08/2021 20:33:04	All	MM Latch-Up Recovery

^{1:} Data acquired during the manoeuvre is flagged as external pointing and not available as nominal data.

Table 4-8 Operational gaps summary

Start	Finish	Data Level	Comments

4.6 Summary of degraded data

In August 2021, SMOS data was affected by the following instrument and processing anomalies which have had a detrimental effect on the data quality.

Table 4-9 Summary of degraded data

Start	Finish	Affected prod- ucts	Problem Description
02/08/2021 07:33:24	02/08/2021 07:53:26	All	CMN Unlock
17/08/2021 09:31:37	17/08/2021 09:41:37	All	CMN Unlock
30/08/2021 20:27:15	30/08/2021 20:37:15	All	CMN Unlock

4.7 Product Quality Disclaimers

The following product disclaimers affects the data generated in the reporting period:

Table 4-10 Summary of product quality disclaimers

Date	Product level	
Whole mission	L1B	Direct_Sun_Correction_Type inside MIR_SC_F1B headers is incorrectly defined. Its value should be 4, which relates to "Correction by Measurements. BT Self-estimation with improved Sun Position Estimate Technique"
From: 21 st June 2017 (06:05:53z) To: 21 st June 2017 (07:28:07z)	L1	Due to CCU reset side effect science data was acquired with instrument pointing in external target looking at deep sky.



5 LONG-TERM ANALYSIS

5.1 Calibration Analysis

Since Level 1 product version v724, the dataset is processed with a fixed NIR noise temperature set to +424.41 [K] for H-Pol and +416.12 [K] for V-Pol. The impact is a more long-term stable Brightness Temperature measurements. The NIR calibration is performed only for monitoring purpose and results are presented in this section.

During the reporting period, there have been two Warm-NIR calibration events on the 4th and 18th of August 2021.

The evolution of the noise temperature of the reference noise diodes Tna and Tnr computed with processor baseline V724 since March 2011 is shown from Figure 1 to Figure 4. The evolution of the NIR noise temperature parameters, which are related to the internal diode stability, are stable in particular for the NIR-CA. The small deviation in the NIR calibration on 3rd June 2015 and on 2nd August 2017 was due to a Radio Frequency Interference (RFI) that corrupted the calibration measurement.

Currently, the calibration team is monitoring an ongoing evolution for the NIR CA Tna H-Pol which began end 2019. The origin of this evolution could be either internal reference diode power evolution or inaccuracy in the antenna losses thermal compensation during calibration. With reduced magnitude the effect is also present for the NIR CA Tna V-Pol.

5.2 Seasonal Evolution of the Calibration Parameters

The Tna and Tnr present in the previous processor baseline V5xx (see for example the monthly report for April 2015) had been largely mitigated by the new calibration algorithm, which decouples the variation of the antenna losses and the drift of the reference diode. This approach allows compensating each drift separately improving the diode stability monitoring and increasing the accuracy of the consequent calibration correction. Further improvements in the calibration stability were achieved by implementing the "Warm-NIR calibration" since 15th of October 2014. During "Warm-NIR calibration", the Noise Injection Radiometer (NIR) calibration is performed with a Sun elevation of 10 degrees above the antenna plane in order to maintain a stable thermal environment of the instrument through the calibration sequence.

Figure 5 and Figure 6 present the evolution of the NIR Observed Brightness Temperature (BT) since the beginning of the mission for V724 baseline. The small variation of a few Kelvins, in the observed BT are due to slightly different regions of the Sky sensed during the calibration manoeuvre. This parameter is used only for monitoring purpose.

The leakage and cross-coupling factors of the NIR channels shown in Figure 7 and Figure 8 remain small and no problems can be observed apart from a peak in the phase of the NIR-AB cross-coupling term on 11 April 2012. That peak corresponds to an anomaly in the NIR-AB that did not have impact on the data.

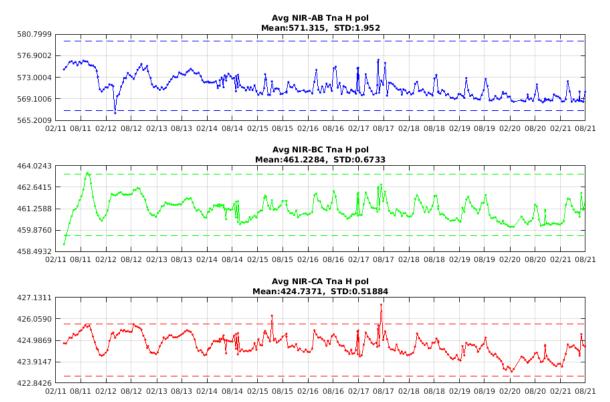


Figure 1 Tna evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the H-channel since the beginning of the mission. Thresholds in dashed lines

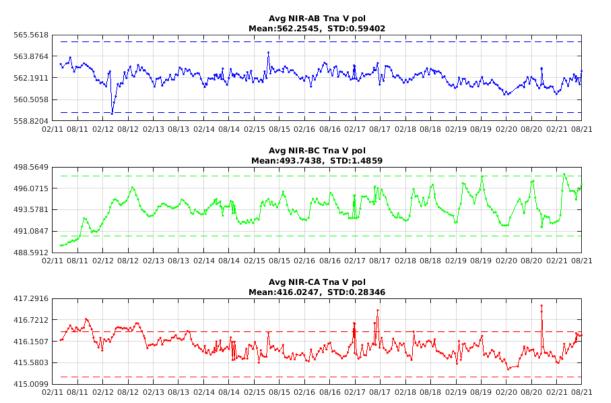


Figure 2 Tna evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the V-channel since the beginning of the mission. Thresholds in dashed lines

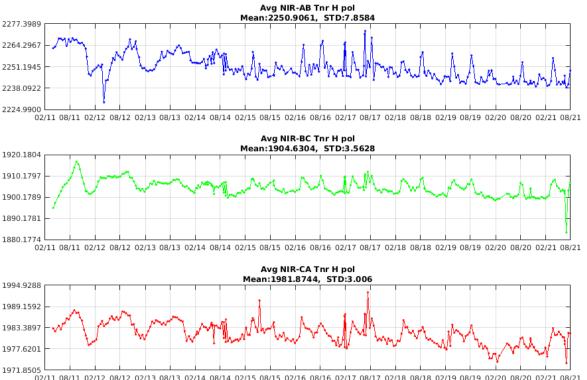


Figure 3 Tnr evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the H-channel since the beginning of the mission.

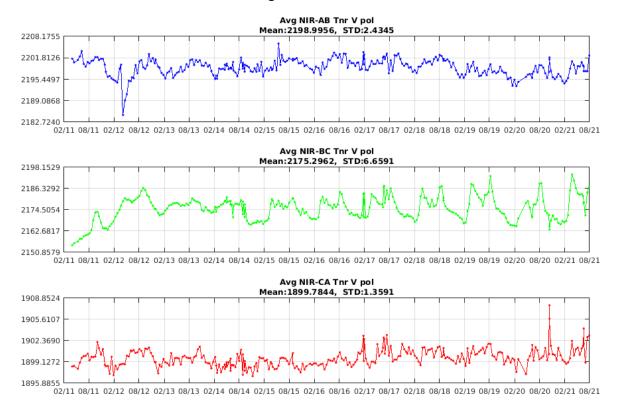


Figure 4 Tnr evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the V-channel since the beginning of the mission.

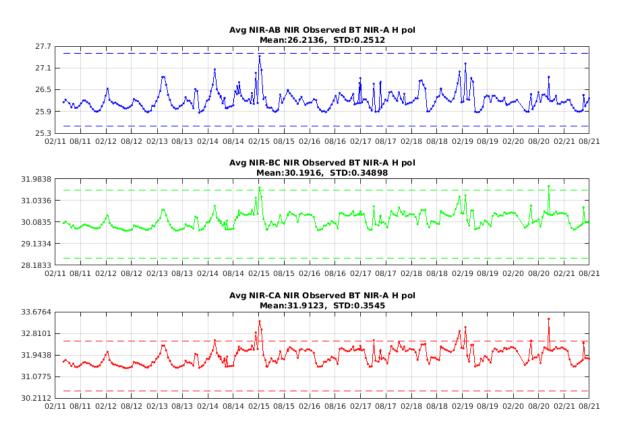


Figure 5 NIR Observed BT evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the H-channel since the beginning of the mission. Thresholds in dashed lines

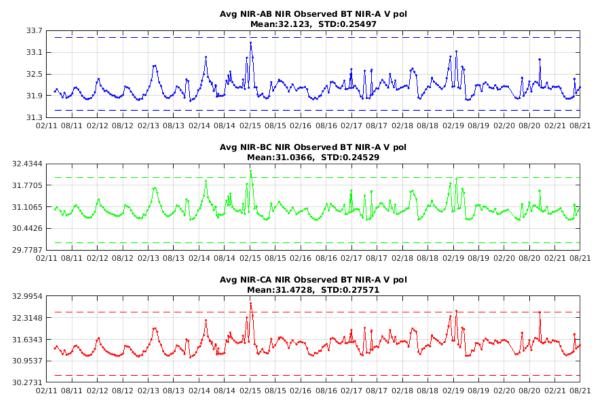
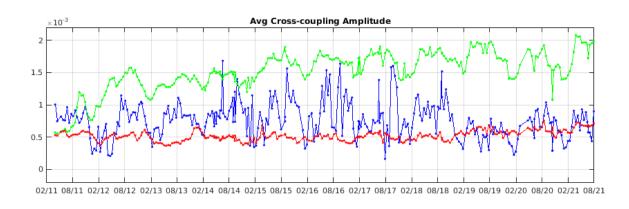


Figure 6 NIR Observed BT evolution of NIR AB (blue), NIR BC (green) and NIR CA (red) in the V-channel since the beginning of the mission. Thresholds in dashed lines



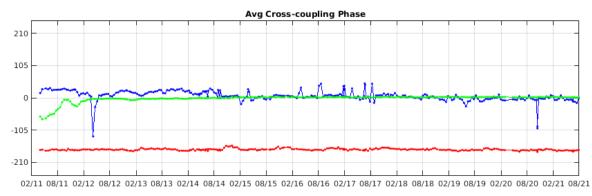
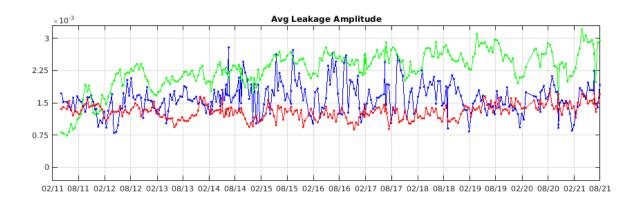


Figure 7 Cross-coupling evolution in amplitude and phase of NIR AB (blue), NIR BC (green) and NIR CA (red) since the beginning of the mission



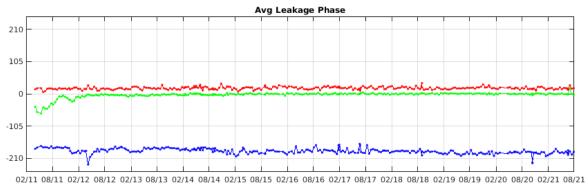


Figure 8 Leakage factor evolution in amplitude and phase of NIR AB (blue), NIR BC (green) and NIR CA (red) since the beginning of the mission

LICEFs

The Lightweight Cost Effective Receivers (LICEF) calibration status is updated by long (every 8 weeks) and short (weekly) on-board calibration activities. No Long calibration were executed during the reporting period.

LICEF PMS gain is derived during the long calibration activity and Figure 9 to Figure 20 show the evolution (V724 algorithm baseline) of the deviations of the PMS gain with respect to its average over time. Note that PMS gain depends on the physical temperature of the receivers; PMS calibration is performed at slightly different physical temperature due to calibration time (season effect) and position of the receiver (LICEF) in the instrument (arms and central hub). In order to compare the calibration results the gains and offsets obtained during the calibration are normalised to 21 degrees Celsius temperature by using the receiver PMS gain and offset temperature sensitivity parameter (one value for each LICEF).

Apart from receiver (LICEF) LCF_A_18, LCF_C_11, LCF_C_19, which have shown a clear evolution from the main trend (see Figure 12, 19, 20) the others PMS gains are stable. The seasonal PMS gain variation present in some LICEFs for previous product version 6 has been mitigated in the current product version 7 by a better characterization of the PMS gain temperature sensitivity parameters. Few LICEFs still present seasonal PMS variation which needs further refinement for the sensitivity parameter.

Figures from Figure 21 to Figure 32 show the evolution of the PMS offsets (V724 algorithm baseline) derived during the short calibration activity.

Figure 33 shows the evolution of the average overall the baselines of the Fringe Washing Function (FWF) amplitude in the origin derived during the long calibration. The amplitude of the FWF at the origin shows a small drift since the beginning of the mission, nevertheless the values are inside the ranges defined in the routine calibration plan.

The evolution of the visibility average offsets (Figure 34 and Figure 35) had an unexpected peak on the 2nd of February 2017. Accordingly to preliminary analysis, this seems related to RFI. The quality impact on the data is small with a peak-to-peak bias of about 0.1K in brightness temperature.

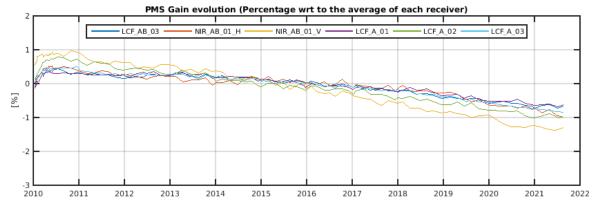


Figure 9 Evolution of the Δ PMS Gain of the LICEFS in CMN H1

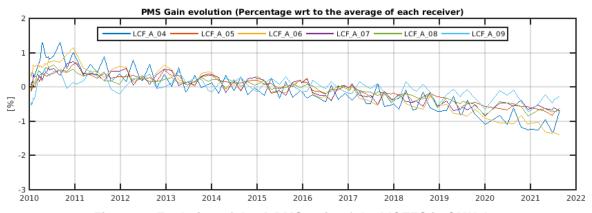


Figure 10 Evolution of the Δ PMS Gain of the LICEFS in CMN A1

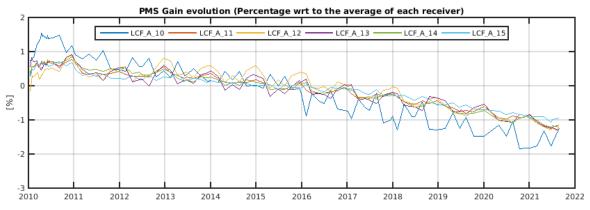


Figure 11 Evolution of the Δ PMS Gain of the LICEFS in CMN A2

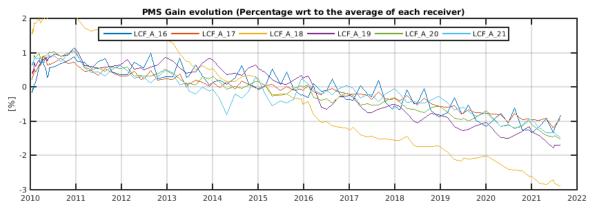


Figure 12 Evolution of the Δ PMS Gain of the LICEFS in CMN A3

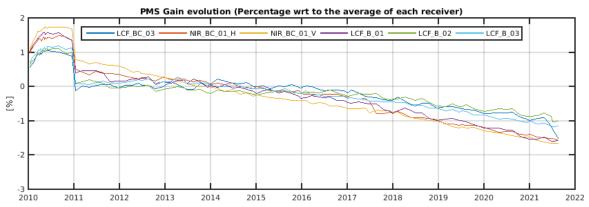


Figure 13 Evolution of the Δ PMS Gain of the LICEFS in CMN H2

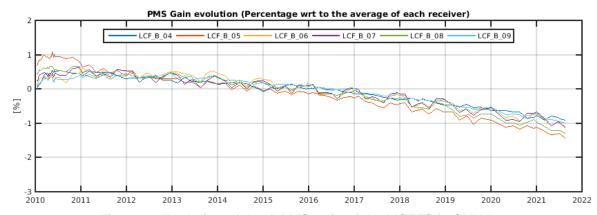


Figure 14 Evolution of the Δ PMS Gain of the LICEFS in CMN B1

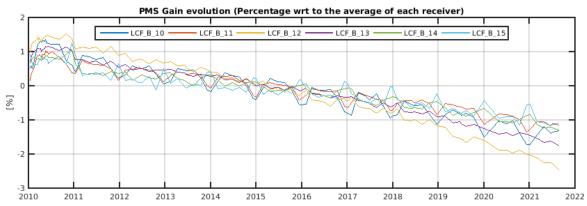


Figure 15 Evolution of the Δ PMS Gain of the LICEFS in CMN B2

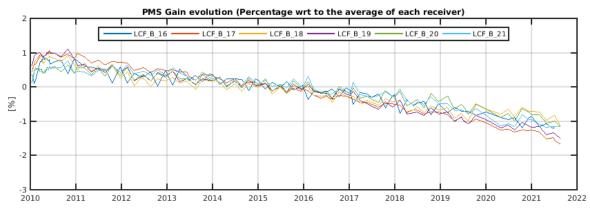


Figure 16 Evolution of the Δ PMS Gain of the LICEFS in CMN B3

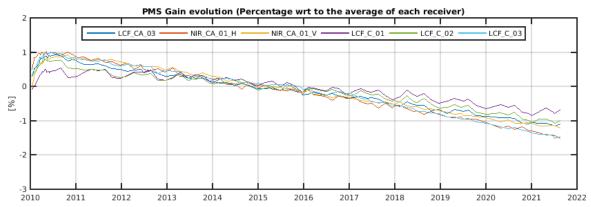


Figure 17 Evolution of the Δ PMS Gain of the LICEFS in CMN H3

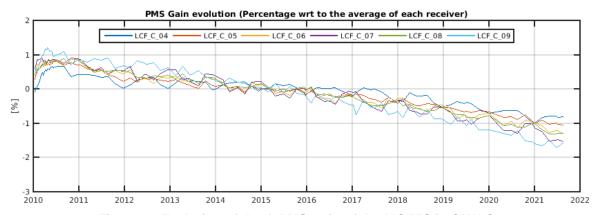


Figure 18 Evolution of the Δ PMS Gain of the LICEFS in CMN C1

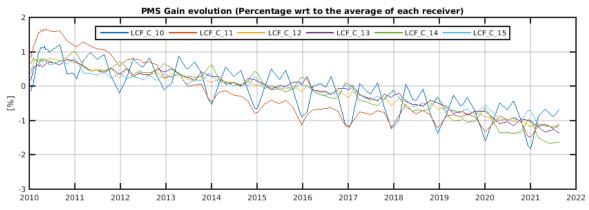


Figure 19 Evolution of the Δ PMS Gain of the LICEFS in CMN C2

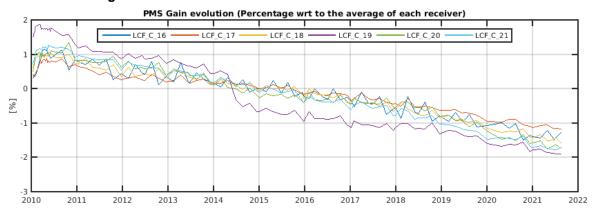


Figure 20 Evolution of the Δ PMS Gain of the LICEFS in CMN C3

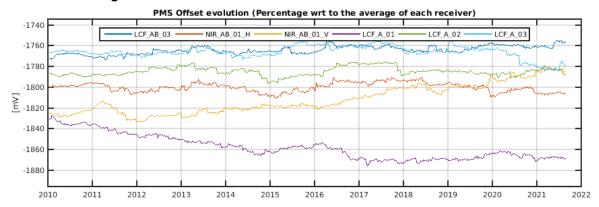


Figure 21 Evolution of the Δ PMS Offset of the LICEFS in CMN H1

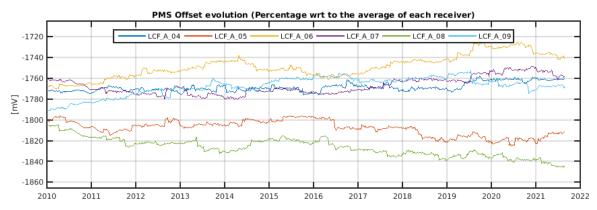


Figure 22 Evolution of the Δ PMS Offset of the LICEFS in CMN A1

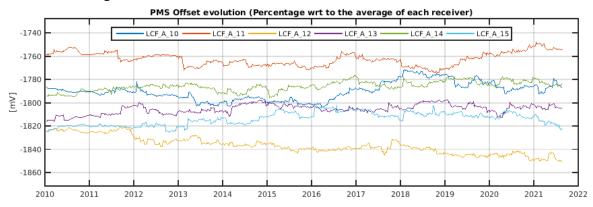


Figure 23 Evolution of the Δ PMS Offset of the LICEFS in CMN A2

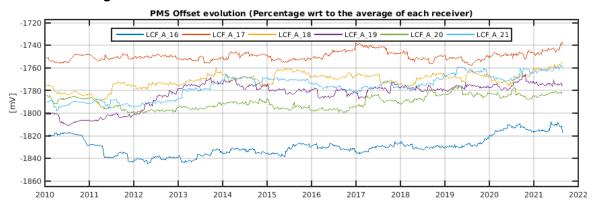


Figure 24 Evolution of the Δ PMS Offset of the LICEFS in CMN A3

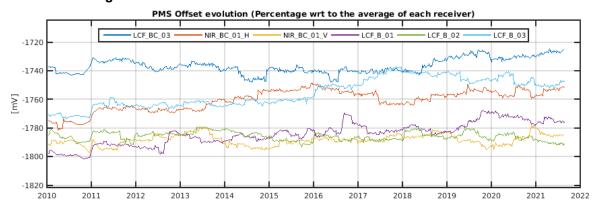


Figure 25 Evolution of the Δ PMS Offset of the LICEFS in CMN H2

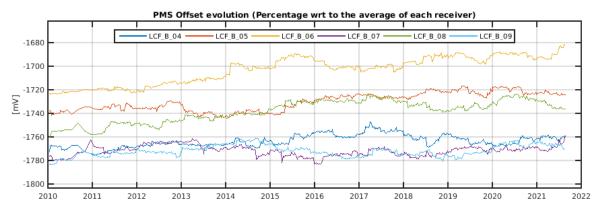


Figure 26 Evolution of the Δ PMS Offset of the LICEFS in CMN B1

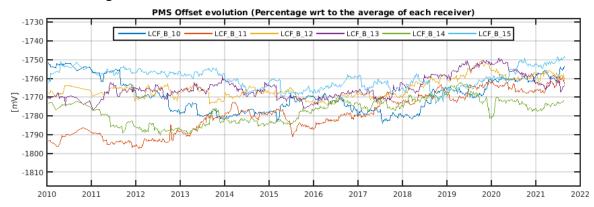


Figure 27 Evolution of the Δ PMS Offset of the LICEFS in CMN B2

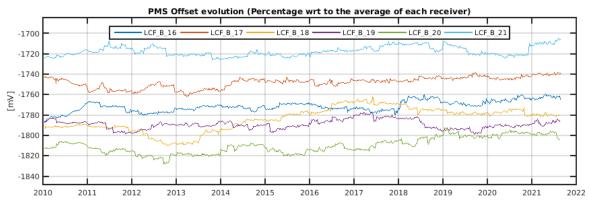


Figure 28 Evolution of the Δ PMS Offset of the LICEFS in CMN B3

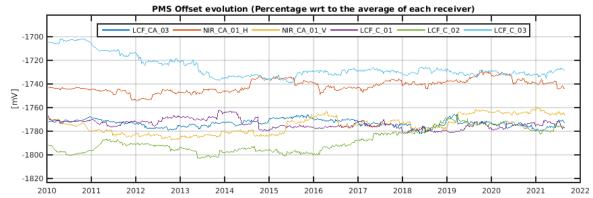


Figure 29 Evolution of the Δ PMS Offset of the LICEFS in CMN H3

-1740

-1760

-1780

-1800

-1820

2010

2021

2022

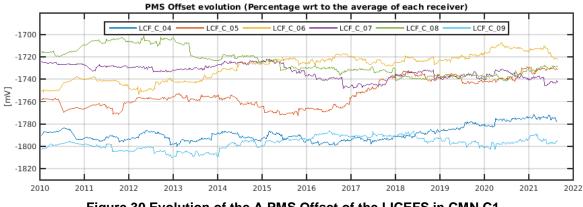


Figure 30 Evolution of the Δ PMS Offset of the LICEFS in CMN C1

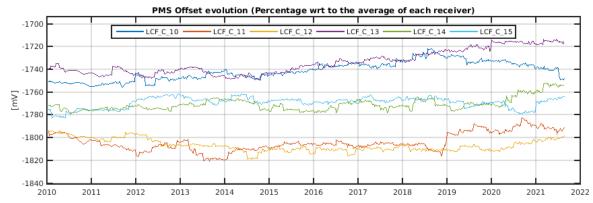


Figure 31 Evolution of the Δ PMS Offset of the LICEFS in CMN C2

PMS Offset evolution (Percentage wrt to the average of each receiver)

LCF.C.16 LCF.C.17 LCF.C.18 LCF.C.19 LCF.C.20 LCF.C.21

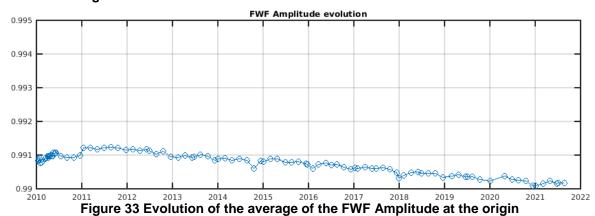
Figure 32 Evolution of the Δ PMS Offset of the LICEFS in CMN C3

2016

2017

2015

2014



The evolution of the average of the correlator offsets does not show any significant drift. Also, the correlation offsets between receivers that do not share local oscillator remains much smaller than

the correlation offsets between receivers sharing local oscillator. This result is expected since any residual correlated signal arriving to a pair of receivers, arrives through the local oscillator signal.

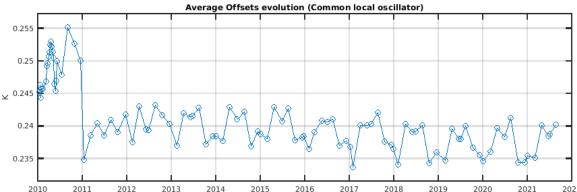


Figure 34 Evolution of the average of the Correlator offsets for the baselines which share local oscillator

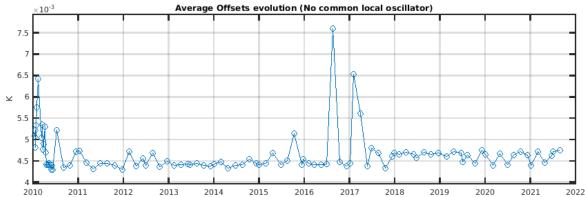


Figure 35 Evolution of the average of the Correlator offsets for the baselines which do not share local oscillator

5.3 Brightness Temperatures Trends over Dome-C Point (Antarctic)

The result of the monitoring of the evolution of the SMOS brightness temperature over Dome-C is shown in the Figure 36 (X and Y polarization at antenna frame for all the incidence angles) and in Figure 37, Figure 38 (H and V polarization at surface level for 42.0 degrees incidence angle for different areas of the Field Of View). The values are averaged every 18 days to reduce the noise and the value for July 2010 is subtracted and used as relative reference. In figure 37 are also shown in situ measurements (Dome-C) from the DOMEX experiment averaged on the same period of the SMOS data. DOMEX data for year 2017 has been calibrated with a more accurate and refined procedure, this explain the bias with reference to previous year acquisition. The residual long term drift in 2017 is due to drift in calibration parameters. Therefore is not a geophysical effect and it will be corrected in the next delivery of DOMEX data.

The evolution of the SMOS brightness temperature trend over Dome-C does not show any significant drift except for two events, in H polarization, at the beginning of 2015 and in March 2020. The increase in Brightness temperature in 2015 was due to a change on surface geophysical condition: accumulation of snow since November 2014 and rapidly evolution of snow density on 22 March 2015 when a strong wind had changed the surface condition. This event has impacted the emissivity of the ice that was confirmed by on-site L-band measurement (DOMEX experiment) and from the Aquarius data set. The decrease in Brightness Temperature in March 2020 is under monitoring. It could be due to change in surface geophysical condition, Preliminary analysis of SMAP data over the same area shows similar trend in H polarisation. A

comparison with on-site L-band measurement (DOMEX experiment) is on-going to better understand the case.

The SMOS brightness temperature V polarization measurements are quite stable since the beginning of the mission. The SMOS brightness temperature H polarization measurements are less stable and impacted by geophysical condition at surface level. Thus, the drop of the latter for the reporting month is under investigation.

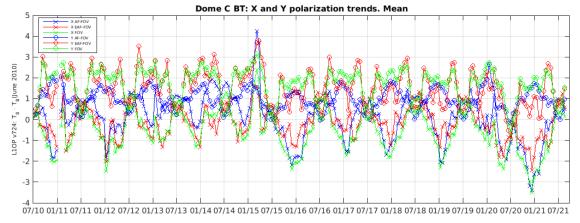


Figure 36: Dome-C X and Y polarization trends (all incidence angles). FOV: statistics computed for Dome-C grid point located in any position of the retrieved image (excluding alias images borders). AF: statistics computed for Dome-C grid point located only in the position of the retrieved image not impacted by alias (alias free area). EAF-FOV: statistics computed for Dome-C grid point located only in the position of the retrieved image where the alias free area has been extended.

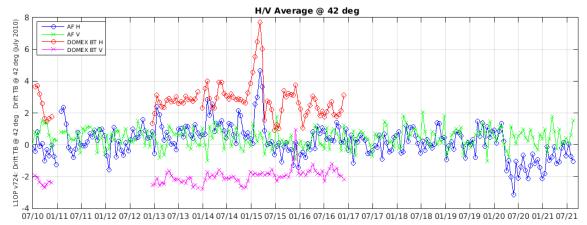


Figure 37: Dome-C H and V polarization trends in Alias Free zone (incidence angle 42°)

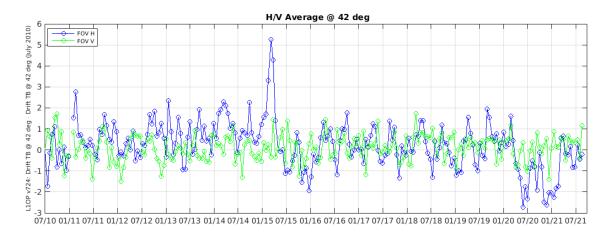


Figure 38: Dome-C H and V polarization trends in Extended Alias Free zone (incidence angle 42°)

5.4 Brightness Temperature Stability over the ocean:

SMOS Brightness Temperature Stability over the ocean is monitored by comparison of SMOS measurements with the forward ocean model. The initial monitoring based on Sea Surface Salinity derived by monthly fixed map from World Ocean Atlas model (WOA2009) has been upgraded with the usage of In-Situ Analysis System (ISAS) measurements interpolated by Objective Analysis (OA). With the new approach, geophysical effects in the difference between SMOS measurements and ocean model has been greatly mitigated allowing a better instrument calibration monitoring. Due to the complexity of the monitoring and the off-line availability of ISAS-OA dataset, the results are available till March 2020. Results will be updated on a yearly basis.

The result of the monitoring of the evolution of the SMOS brightness temperature over the ocean is shown in the Figures 40-43 as a Hövmoller plot (time-latitude plot with averaged longitudes for the Brightness Temperature anomaly with respect to the ocean model).

The latitude-longitude area is defined as described in figure 39. This aims to obtain a sufficiently large water body without much interfering land masses, land sea contamination, RFI presence, etc, to be used as a well-known reference. For that area, the ocean model is deemed sufficiently known.

In addition to the Hövmoller plots, several additional metrics are provided. Figures 44-47 contain trends computed over the Hövmoller for several areas of interest. They contain latitude-longitude Brightness Temperature averages evolution.

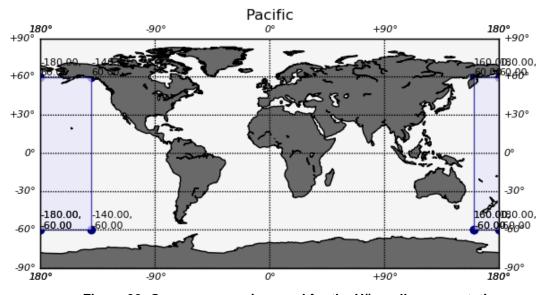


Figure 39: Open ocean region used for the Hövmoller computation.

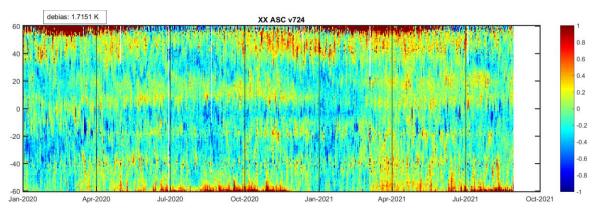


Figure 40: BT stability over the ocean, for XX polarization and Ascending passes in Kelvin.

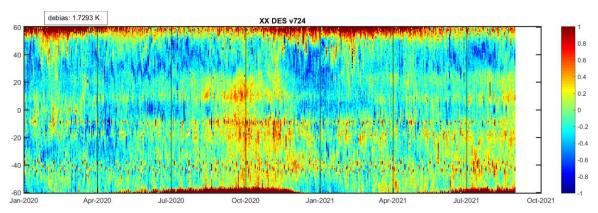


Figure 41: BT stability over the ocean, for XX polarization and Descending passes in Kelvin

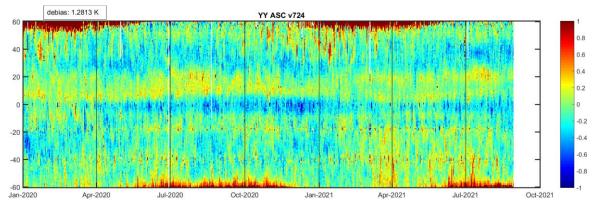


Figure 42: BT stability over the ocean, for YY polarization and Ascending passes in Kelvin.

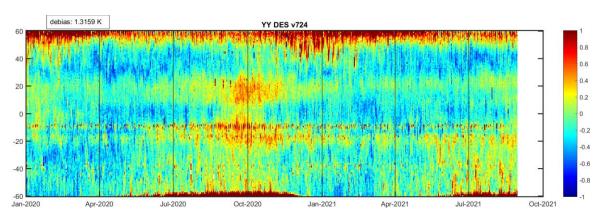


Figure 43: BT stability over the ocean, for YY polarization and Descending passes in Kelvin.

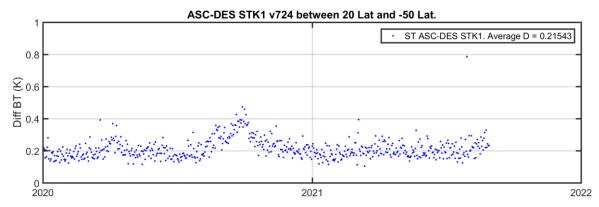


Figure 44: BT short-term stability trends (ASC-DES) for Stokes 1, XX and YY polarizations

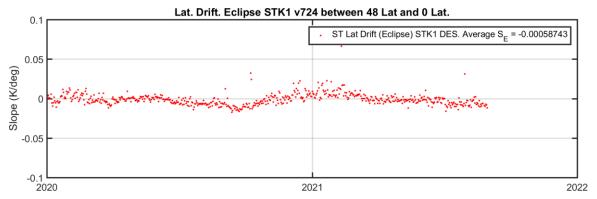


Figure 45: BT short term stability at Eclipse regions, for Stokes 1, XX and YY polarizations

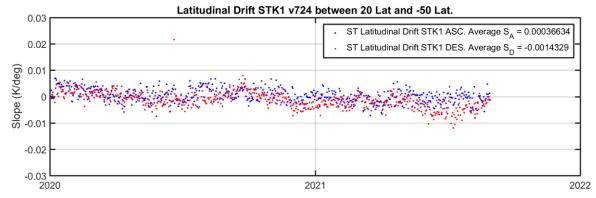


Figure 46: BT short term stability (Latitudinal drift) for ASC-DES Stokes 1, XX and YY polarizations.

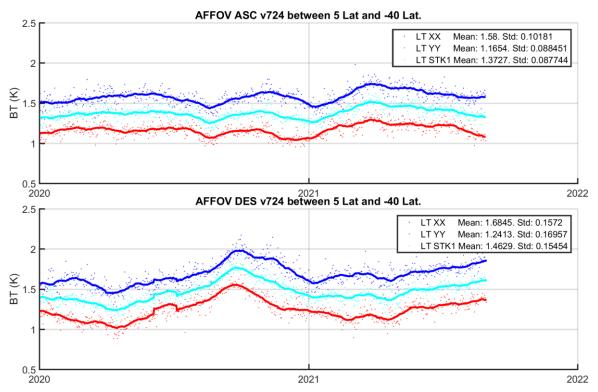


Figure 47: BT long term stability (ASC/DES), for XX and YY polarizations, and Stokes 1.

5.5 L1C Quality Parameter Analysis

This section provides a long-term overview of the monitoring of the L1C quality parameters, in order to identify major problems and trends.

The L1C quality flags have been extracted for the whole dataset. The L1C quality flags identify the total number of degraded measurements (snapshots) which have been affected by software, instrument, auxiliary data (ADF) or calibration errors. They provide a way to assess the degradation of a particular measurement and product and to help to identify its causes.

A daily average of the number of degraded measurements per MIR_SCxx1C file (product) has been selected as a metric. This provides an estimation of the mean number of degraded snapshots per file (product) for each day:

 $\label{eq:mean number of degraded snapshot per product} \begin{aligned} & \frac{\text{total number of degraded snapshots per day}}{\text{total number of products per day}} \end{aligned}$

The metric is also given in relative percentage with respect the total number of snapshots in that day:

Percentage of degraded snapshots per day = $100 \times \frac{\text{total number of degraded snapshots per day}}{\text{total number of snapshots that day}}$

The metric is computed for ascending and descending orbit direction separately. In case the percentage of degraded snapshots per day is not negligible (e.g. above 2%) a detailed analysis of the dataset is performed in order to identify the root cause of the degradation for that day.

5.5.1 **Software Errors**

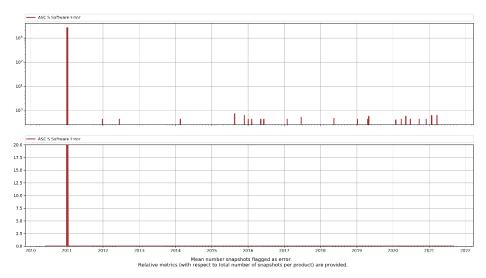


Figure 48: L1C V724 Software Errors Ascending Orbits

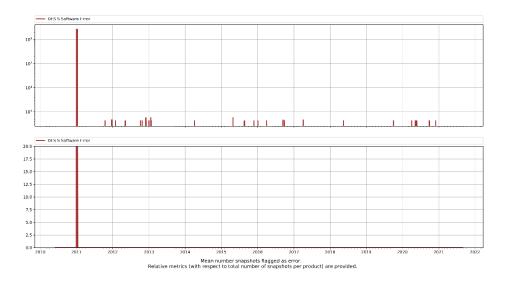


Figure 49: L1C V724 Software Errors Descending Orbits

This error summarizes several exceptions that the SW may encounter, such as interpolation problems, denominator equal to 0, out-of-bound parameters, etc (see **RD1** for more comprehensive detailed description). Only few software errors were logged for the entire dataset. The exception is Jan 2011, which were originated on the instrument anomaly, and May 2010, during commissioning phase (origin under investigation). After Jan 2011, the number of affected snapshots is negligible.

5.5.2 Instrument Errors

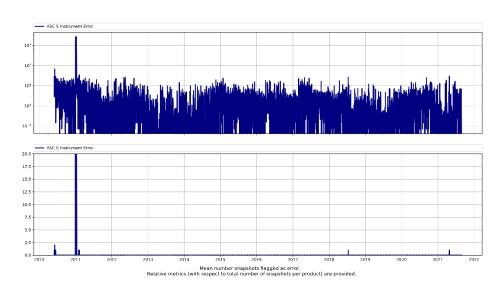


Figure 50: L1C V724 Instrument Errors Ascending Orbits

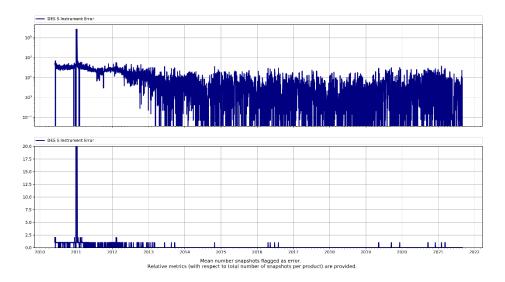


Figure 51: L1C V724 Instrument Errors Descending Orbits

This error signals instrument anomalies that could have impact in data quality (see **RD1** for more comprehensive detailed description). In addition, it has been discovered that severe RFI may trigger this error flag as well: In some cases, RFIs sources strongly degrades the instrument correlation measurement. This degradation impacts the number of iterations required to converge in the internal L1A correlation correction and therefore is flagged as potential instrument error and further propagated to L1C product.

Besides commissioning period (May 2010) and the instrument anomaly in January 2011, the number of flagged snapshots is very low, always being < 2.5%. An interesting drift is observed for the Instrument Error flag for Descending passes, as it clearly shows a progressive reduction of the number of corrupted snapshots from an average of 50 affected snapshots per day to an average of five. This can be related with the removal of on ground RFIs sources with respect the beginning of the mission. Interesting enough, the same trend cannot be appreciated for Ascending passes. This can be associated with the fact that reduction of RFI was particularly intense for some North American sources that mainly affected descending passes.

5.5.3 ADF Errors

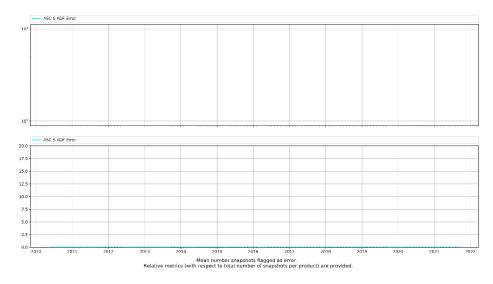


Figure 52: L1C V724 ADF Errors Ascending Orbits

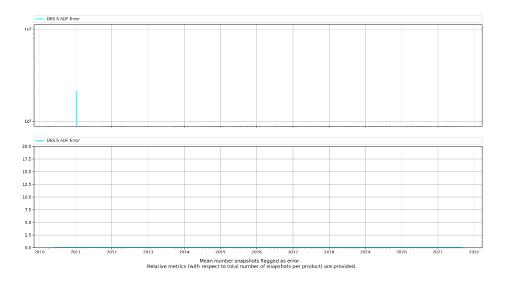


Figure 53: L1C V724 ADF Errors Descending Orbits

ADF Errors count the number of snapshots affected by Auxiliary Data anomalies (see **RD1** for more comprehensive detailed description). No significant number of ADF errors are logged for the entire dataset. The exception is Jan 2011, which were originated on the instrument anomaly. Some other products from 2010 to 2015 present ADF errors coming from no AUX_RFILST data available. This issue only affects some snapshots one product each change of month (see plot below) and it is caused by a non-enough overlapping of the input 2010-2015 AUX_RFILST dataset.

5.5.4 Calibration Errors

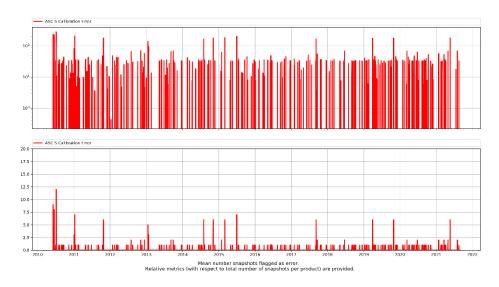


Figure 54: L1C V724 Calibration Errors Ascending Orbits

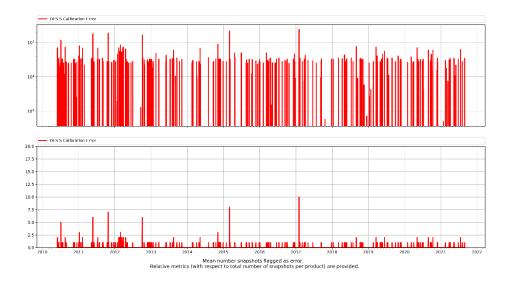


Figure 55: L1C V724 Calibration Errors Descending Orbits

Calibration errors summarize diverse errors related to calibration sequences usage (see **RD1** for more comprehensive detailed description). Calibration errors are fairly constant across the mission, generally affecting less than to the 1% of the sequences every day. In selected dates, however, anomalies that are more serious can be appreciated, affecting up to 5% of the snapshots.

For the rest of the dataset, calibration errors are commonly triggered due to events causing gaps in the Local Oscillator calibrations (CSTD1A) or degraded calibration data due to CMN Unlocks. Specific detail for each product affected by calibration errors can be found in RD1 inside 'ALL FULL Degraded Products' section. Additionally, it should be mentioned that most of the products proposed inside the 'Main FULL Degraded Products' section to be discarded are the ones affected by calibrations errors.

5.5.5 Invalid Blocks

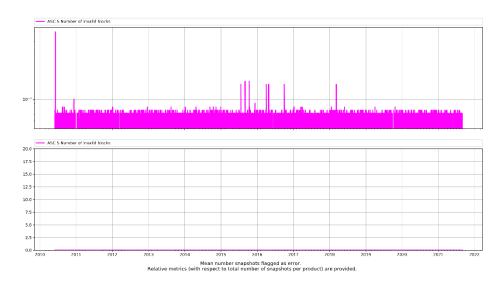


Figure 56: L1C V724 Invalid Blocks Ascending Orbits

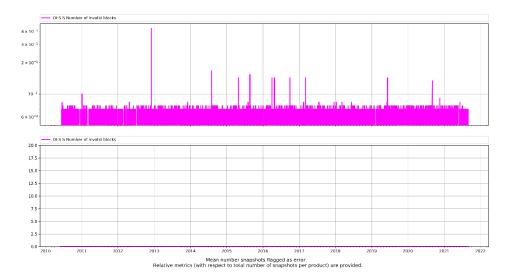


Figure 57: L1C V724 Invalid Blocks Descending Orbits

The invalid blocks flag counts the number of blocks of 24 packets in the corresponding L0 product that have at least one invalid packet (see **RD1** for more comprehensive detailed description). Albeit some packets are regularly flagged as invalid, the number of flagged packets is negligible for the entire dataset.

5.5.6 Discarded Scenes

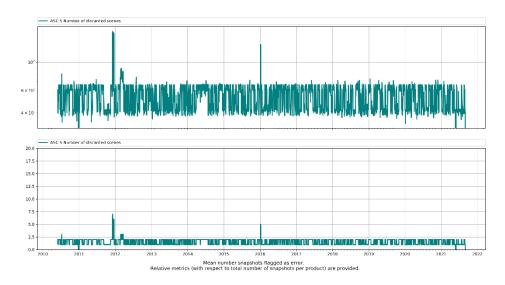


Figure 58: L1C V724 Discarded Scenes Ascending Orbits

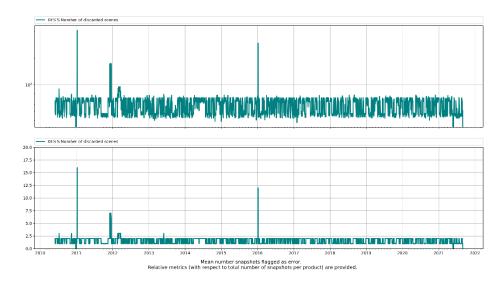


Figure 59: L1C V724 Discarded Scenes Descending Orbits

The number of discarded scenes counts the amount of snapshots discarded at all the processing stages (L0, L1A, L1B and L1C). An snapshot may be discarded due to different reasons (see RD1 for more comprehensive detailed description). The number of discards remain fairly constant (around 60 snapshots, < 2.5%) outside the commissioning period and the Jan 2011 anomaly. Nevertheless, two events with higher number of discards have been identified, at Dec 2011 and Jan 2016, respectively:

Between 20111213T173338 and 20111219T002236, 160-170 discards are reported per product.

For two products large discards are reported, SM_REPR_MIR_SCSF1C_20160106T031835_20160106T033333_724_201_1 with 2868 snapshots discarded, and SM_REPR_MIR_SCSF1C_20160106T042338_20160106T050151_724_200_1 with 1184

No clear cause for this has been found yet.

5.6 L2OS Ocean Target Transformation (OTT) Orchestration Analysis

The OTT correction is used by the L2OS processor for sea surface salinity retrieval. The correction is computed roughly on a daily basis by accumulating previous SMOS L1C measurements. The proper usage of the OTT correction is monitored and results are present in **Figure 60** since June 2010. **Figure 60** shows the OTT delay defined as the delta time between the L2OS science product sensing time and the OTT correction validity time and averaged over 1 day period. As the validity time of the OTT correction depends on the dataset used to compute the correction, this OTT delay represents a quality indicator for the selection of the best OTT correction (i.e. the better correction is achieved by using an OTT with validity time closer to the L2OS sensing time).

Nominal OTT delay interval goes from 4 to 8 days of delay. The most of the OTT delays fall in the middle of such values, 5-6 days. OTT delays outside the nominal interval reveals anomalies either in the data selection policy or problems in accumulating L1C dataset (i.e. data rejection due to bad quality or presence of RFI).

For the current SMOS L2OS v700 dataset, the next anomaly periods affecting the OTT delay (i.e. delay above 8 days) have been found:

- 1) From 21/12/2010 to 08/01/2011: Electrical Stability Test and Temperature Reading anomalies with consequent unavailability of L1C data and increased OTT delay
- From 01/04/2014 to 08/04/2014 OTT delays above 8 days due to L1C rejected data for OTT correction. Data rejection was due to corrupted L1C measurement affected by RFI.

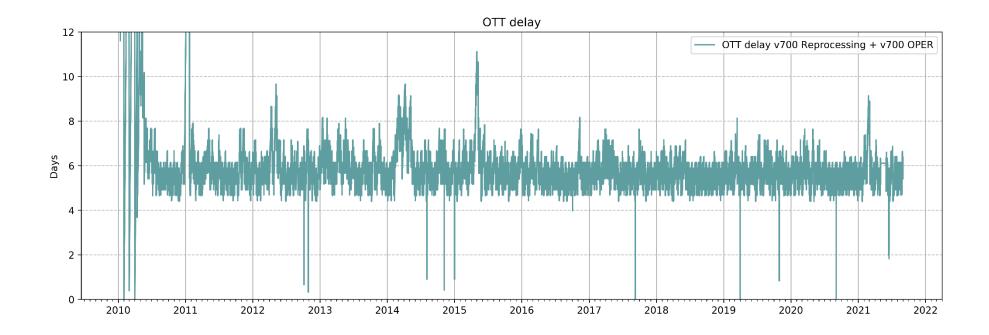


Figure 60: OTT delay per semi-orbit (Delta time between each L2OS product start time and the OTT correction validity start time file).

5.7 L2OS Retrievals assessment

Analysis on the overall quality of the L2OS dataset is based on the evolution of the number of 'good quality' retrievals as shown in RD1 (ascending orbits) and in Figure 62 (descending orbits) as reported in the product header.

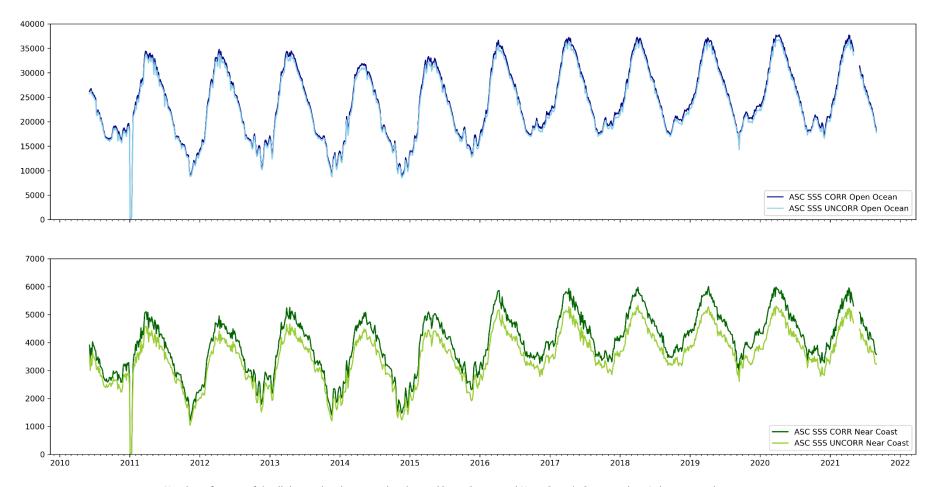
These 'Good Quality' retrievals are taken into account for two different areas: Open Ocean (more than 800km away from coastline) and Near Coast (within 800 km from the coastline).

Also, retrievals have been computed for the land-sea contamination corrected and uncorrected Sea Surface Salinity (SSS_corr, SSS_uncorr) and averaged on a daily basis, providing an estimation of the average number of retrievals per product. The seasonal variation in the number of good retrieval is mainly due to the criteria used to classify the data. This criteria is based on the following flags contained in the product:

- fg ctrl many outliers
- fg_ctrl_sunglint
- fg_ctrl_moonglint
- fg_ctrl_gal_noise
- fg_ctrl_num_meas_low
- fg sc suspect ice
- fg_sc_rain
- fg_sc_TEC_gradient

This criteria, will be reviewed in the next version of the L2OS processor and aligned with the "good quality" criteria recommended by the Expert Support Laboratory based on the following flags contained in the product:

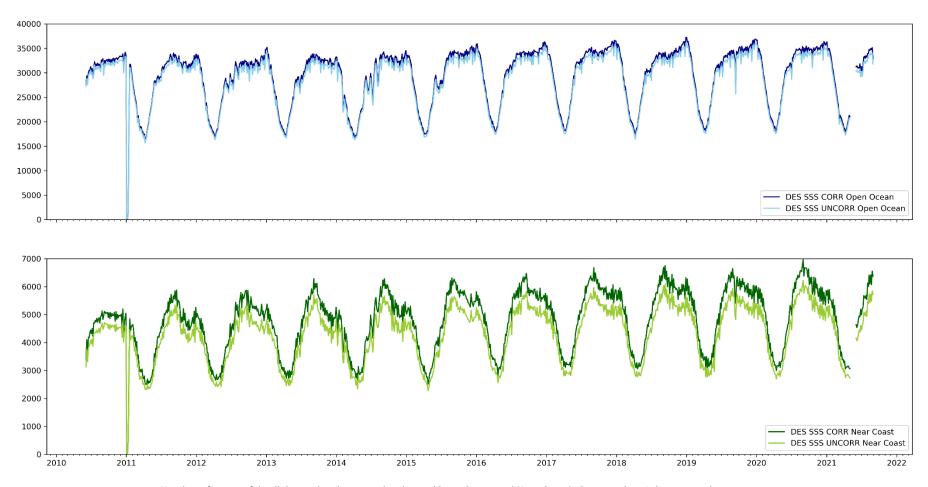
- fg ctrl range
- fg_ctrl_sigma
- fg_ctrl_chi2
- fg_ctrl_chi2_P
- fg_ctrl_marq
- fg_ctrl_reach_maxiter



Number of successful salinity retrievals per retrieval case (Open Ocean and Near Coast). Computed as 4-day per product average.

Figure 61 ASC Open Ocean and Near Coast L2OS Good Quality Retrievals





Number of successful salinity retrievals per retrieval case (Open Ocean and Near Coast). Computed as 4-day per product average.

Figure 62 ASC Open Ocean and Near Coast L2OS Good Quality Retrievals

5.8 L2SM Retrievals assessment

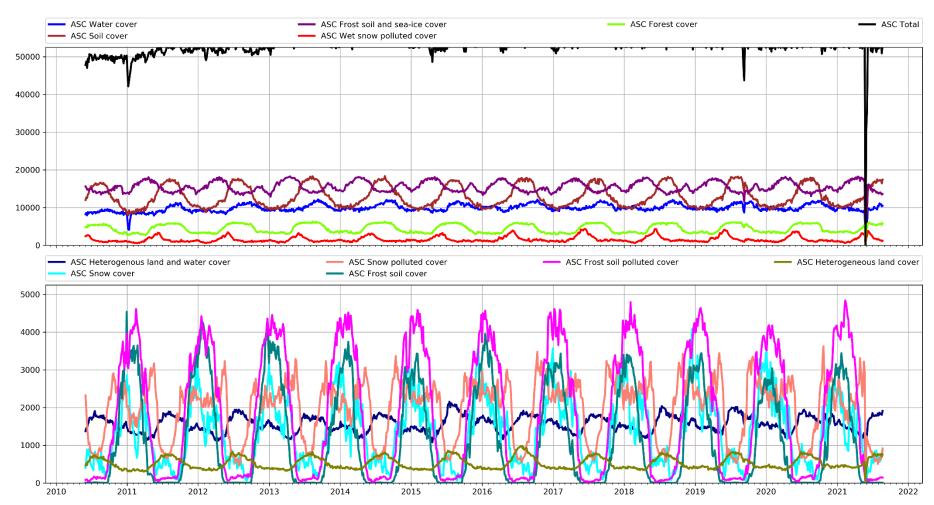
Analysis on the overall quality of the L2SM v700 dataset is based in the number of successful retrievals annotated in the SMUDP2 header file.

Such parameter is extracted for each retrieval branch. For some of the retrieval branches (i.e. Soil and Forest cover) this means a successful Soil Moisture retrieval. For the rest of branches, however, the parameter retrieved could be surface dielectric constant, optical depth, surface roughness or surface temperature. Please, refer to L2SM processor product specification for more details at this respect.

The metric is aggregated every 4 days in order to remove rapid variations originated due to geophysical changes in the surface. Also, it is provided as an average value per product, both in absolute value and in percentage with respect the total retrievals per branch. The metric is computed separately between ASC and DES semi-orbits, as the time of the overpass is different (ascending pass equator crossing at 06.00UTC a.m., descending pass equator crossing at 06.00UTC p.m.).

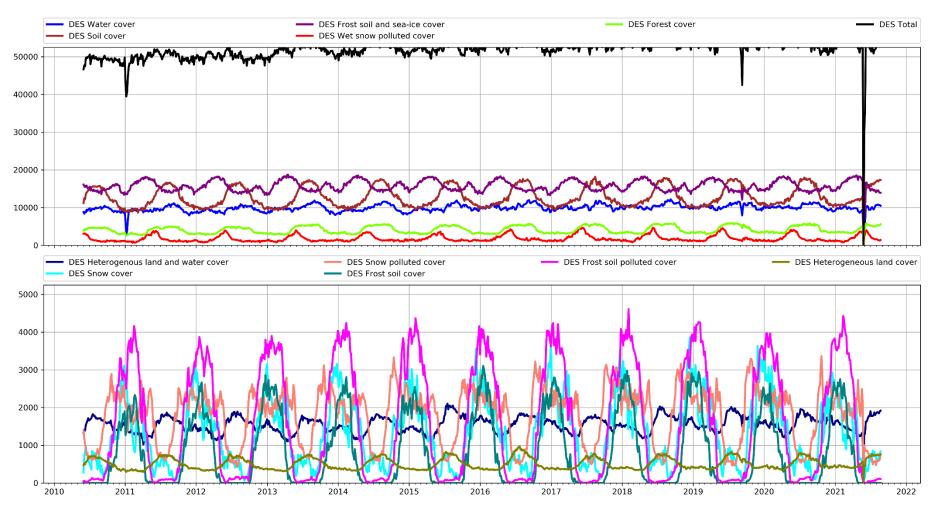
An increase on the number of retrievals for the 3 first years of operations is apparent. The origin of this is the reduction of RFI sources as a consequence of reporting the RFI case to the Spectrum Management Authorities since launch. In addition, v700 shows a higher number of retrievals with respect to v700. This is expected due to the change in the land cover auxiliary information especially relevant for Forest cover, but it is also apparent for other retrieval branches (e.g. Soil).

The relative total number of successfully retrievals presents some seasonal behaviour specially for descending semi-orbits. For some of the parameters (i.e. Forest, Snow) this bearing is especially clear for both ascending and descending, and may be related with surface changes across the seasons.



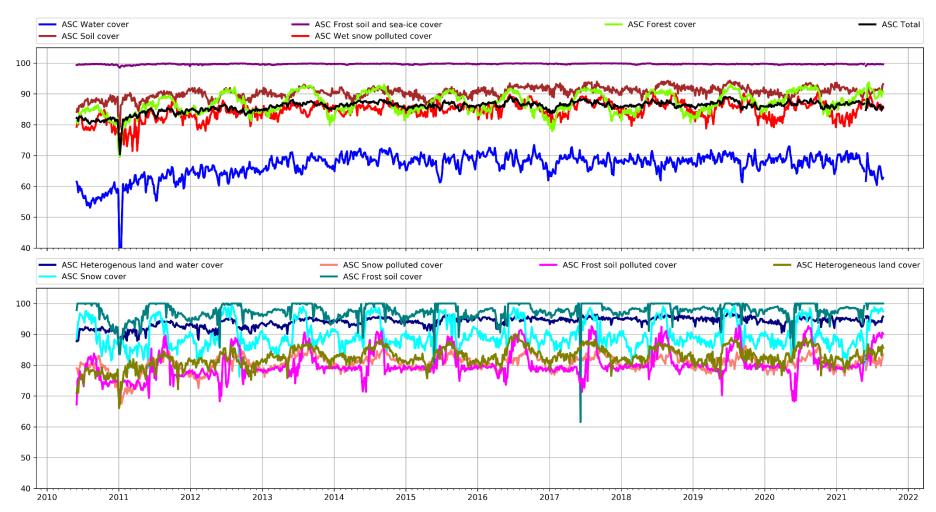
Mean number of nodes with successful retrieval of the surface dielectric constant per retrieval case. Computed as 4-day per product average. For cases: Soil and Forest cover, Soil Moisture is also successfully retrieved. For the rest of cases, other parameters (optical depth, surface roughtness, surface temperature) might be retrieved.

Figure 63: L2SM v700 Mean Retrievals Absolute - ASC



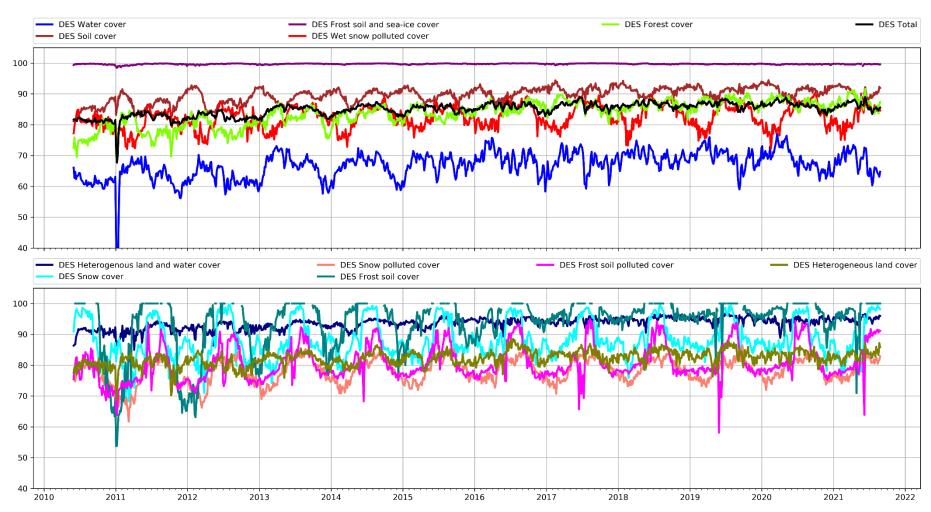
Mean number of nodes with successful retrieval of the surface dielectric constant per retrieval case. Computed as 4-day per product average. For cases: Soil and Forest cover, Soil Moisture is also successfully retrieved. For the rest of cases, other parameters (optical depth, surface roughtness, surface temperature) might be retrieved.

Figure 64: L2SM v700 Mean Retrievals Absolute - DES



% number of nodes with successful retrieval of the surface dielectric constant per retrieval case, with respect to the total number of nodes for such retrieval case. Computed as 4-day per product average. For cases: Soil and Forest cover, Soil Moisture is also successfully retrieved. For the rest of cases, other parameters (optical depth, surface roughtness, surface temperature) might be retrieved.

Figure 65: L2SM v700 Mean Retrievals Relative - ASC



% number of nodes with successful retrieval of the surface dielectric constant per retrieval case, with respect to the total number of nodes for such retrieval case. Computed as 4-day per product average. For cases: Soil and Forest cover, Soil Moisture is also successfully retrieved. For the rest of cases, other parameters (optical depth, surface roughtness, surface temperature) might be retrieved.

Figure 66: L2SM v700 Mean Retrievals Relative - DES

IDEAS-QA4E0

Pi-MEP: SSS Time series with Argo Buoys.

This section presents the systematic analysis of the Sea Surface Salinity difference between SMOS Level 2 measurements and in-situ (Argo buoys) measurements, specifically the time series of the monthly median and standard deviation of them from the SMOS Pi-MEP. For more information about the match-up database used to derive the differences please see the full monthly report from the SMOS Pilot-Mission Exploitation Platform for SSS available here (https://pimep.ifremer.fr/diffusion/smos-l2-v700 monthly-update/).

SMOS Pi-MEP is a project funded by ESA focused on validation of various satellite derived SSS products. The project gathers together European expertise groups (IFREMER, OceanDataLab, OceanScope) as well as NASA Expert Laboratories. For more detail on the project, please visit the SMOS Pilot-Mission Exploitation Platform (Pi-MEP).

Ascending and Descending Orbits.

For Figure 67:

- The top panel shows the time series of the monthly median SSS estimated over the full Global Ocean Pi-MEP region for both SMOS SSS L2 v700 satellite SSS product (in black) and the Argo in situ dataset (in blue) at the collected Pi-MEP match-up pairs.
- The middle panel shows the time series of the monthly median of ΔSSS (Satellite - Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.
- The bottom panel shows the time series of the monthly standard deviation of the ΔSSS (Satellite - Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.

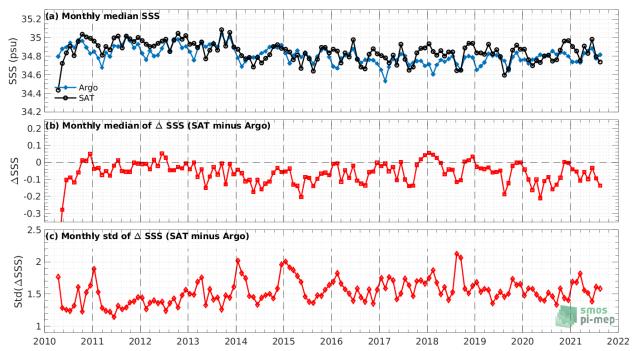


Figure 67 - Time series of the monthly median SSS (top), median of ΔSSS (Satellite - Argo) and Std of ΔSSS (Satellite - Argo) over the Global Ocean Pi-MEP region considering all match-ups collected by the Pi-MEP.

Ascending Orbits

For Figure 68:

- The *top panel* shows the time series of the monthly median SSS estimated over the full Global Ocean Pi-MEP region for both SMOS SSS L2 v700 satellite SSS product (in black) and the Argo in situ dataset (in blue) at the collected Pi-MEP match-up pairs.
- The *middle panel* shows the time series of the monthly median of ΔSSS (Satellite Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.
- The *bottom panel* shows the time series of the monthly standard deviation of the ΔSSS (Satellite Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.

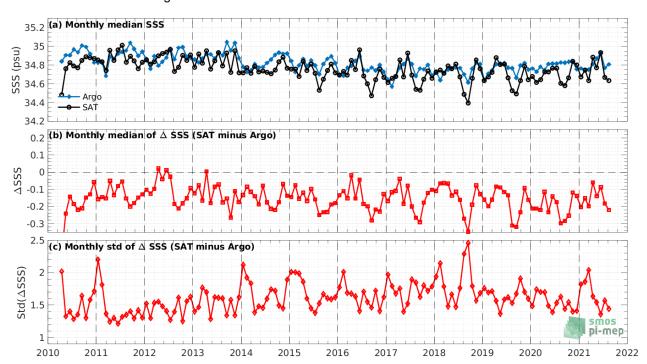


Figure 68 - Time series of the monthly median SSS (top), median of ΔSSS (Satellite - Argo) and Std of ΔSSS (Satellite - Argo) over the Global Ocean Pi-MEP region considering only ascending orbits from all match-ups collected by the Pi-MEP.

Descending Orbits

For Figure 69:

- The top panel shows the time series of the monthly median SSS estimated over the full Global Ocean Pi-MEP region for both SMOS SSS L2 v700 satellite SSS product (in black) and the Argo in situ dataset (in blue) at the collected Pi-MEP match-up pairs.
- The middle panel shows the time series of the monthly median of ΔSSS (Satellite Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.
- The bottom panel shows the time series of the monthly standard deviation of the ΔSSS (Satellite - Argo) for the collected Pi-MEP match-up pairs and estimated over the full Global Ocean Pi-MEP region.

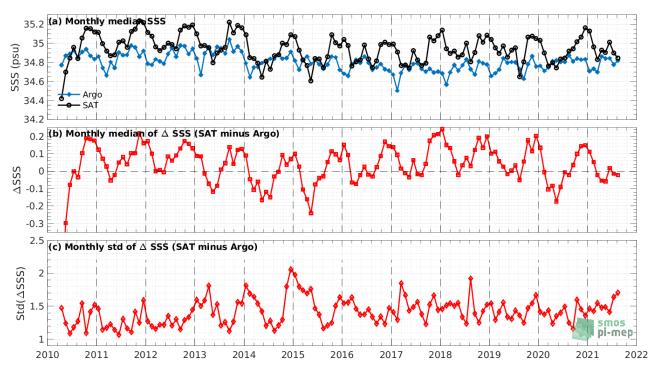


Figure 69 - Time series of the monthly median SSS (top), median of Δ SSS (Satellite - Argo) and Std of Δ SSS (Satellite - Argo) over the Global Ocean Pi-MEP region considering only descending orbits from all match-ups collected by the Pi-MEP.

6 PRODUCT QUALITY ANALYSIS

Level 1 data quality for August has found to be nominal except in the time intervals listed in the section 4.5. Weekly maps for ascending and descending passes for the Stokes 1, Stokes 3 and Stokes 4 in videos format can be found at: https://earth.esa.int/eogateway/instruments/miras/quality-control-reports/smos-videos

All the artificial patterns in the maps can be explained by the presence of RFIs.

Level 2 Soil Moisture data quality for August has found to be nominal. Weekly maps for ascending and descending passes for the soil moisture in videos format can be found at: https://earth.esa.int/eogateway/instruments/miras/quality-control-reports/smos-videos

Level 2 Sea Surface Salinity data quality is nominal in the reporting period. Weekly maps for ascending and descending passes for good retrieved sea surface salinity in videos format can be found at: https://earth.esa.int/eogateway/instruments/miras/quality-control-re-ports/smos-videos

The lack of good retrieval at descending passes during the boreal winter season is less evident for winter season 2015/2016, 2016/2017 and 2017/2018, This fact points out that thermal effect on the instrument due to eclipse is only one contributor and others sources (e.g. L-band Sun signal direct or reflected) impacting the number of good retrieval are under investigation by the calibration team and Level 2 ESL.

For more details on soil moisture and sea surface salinity retrieval algorithms and caveats in data usage see the **Level 2 Algorithm Theoretical Baseline Documents** and the read-me-first note available here:

- L2OS: https://earth.esa.int/eogateway/documents/20142/37627/SMOS-L2OS-ATBD.pdf?text=smos+atdb
- **L2SM:** https://earth.esa.int/eogateway/documents/20142/37627/SMOS-L2-SM-ATBD.pdf?text=smos+atdb



7 ADF CONFIGURATION AT THE END OF THE REPORTING PERIOD

AUX APDL	ADF File Type	Operational ADF Version (DPGS Baseline)	Updated
AUX APDS	AUX_APDL	SM_OPER_AUX_APDL20050101T000000_20500101T000000_300_004_3.EEF	No
AUX ATMOS SM_OPER_AUX_BFP20050101T000000_20500101T000000_340_004_3 EEF	AUX_APDNRT	SM_OPER_AUX_APDNRT_20050101T000000_20500101T000000_208_001_6.EEF	No
AUX BFP	AUX_APDS	SM_OPER_AUX_APDS20050101T000000_20500101T000000_300_004_3.EEF	No
AUX_BNDLST	AUX_ATMOS_	SM_OPER_AUX_ATMOS20050101T000000_20500101T000000_001_010_3.EEF	No
SM_OPER_AUX_BNDLST_20050101T000000_2050101T000000_303_004_3	AUX_BFP	SM_OPER_AUX_BFP20050101T000000_20500101T000000_340_004_3.EEF	No
AUX_BULL_B		SM_OPER_AUX_BNDLST_20160308T000000_20500101T000000_303_004_3	No
AUX CNFAR SM. OPER AUX CNFAR 20050101T000000_20500101T000000_31_0002_3.EEF NO AUX CNFLOP SM. OPER AUX CNFAR 20050101T0000000_20500101T0000000_100_002_3.EEF NO AUX CNFLOP SM. OPER AUX CNFLOP 20050101T0000000_20500101T0000000_001_007_3.EEF NO AUX CNFLOP SM. OPER AUX CNFLOP 20050101T0000000_20500101T0000000_001_007_3.EEF NO AUX CNFLOP SM. OPER AUX CNFLOP 2010206T001000_20500101T0000000_001_007_3.EEF NO AUX CNFNRT SM. OPER AUX CNFNRT 20050101T0000000_20500101T0000000_001_029_3.EEF NO AUX CNFOSD SM. OPER AUX CNFOSD 20050101T0000000_20500101T0000000_001_029_3.EEF NO AUX CNFOSD SM. OPER AUX CNFSMD 20050101T0000000_20500101T0000000_001_029_3.EEF NO AUX CNFOSD SM. OPER AUX CNFSMD 20050101T0000000_20500101T0000000_001_032_3.EEF NO AUX CNFSMD SM. OPER AUX CNFSMD 20050101T0000000_20500101T0000000_001_017_3.EEF NO AUX CNFSMF SM. OPER AUX CNFSMD 20050101T0000000_20500101T0000000_001_017_3.EEF NO AUX CNFSMF SM. OPER AUX DFFRA2 20050101T0000000_20500101T0000000_001_017_3.EEF NO AUX DFFFRA SM. OPER AUX DFFRA2 20050101T0000000_20500101T0000000_001_007_3 NO AUX DFFRA SM. OPER AUX DFFRA2 20201011T0000000_20500101T0000000_001_007_3 NO AUX DFFRA SM. OPER AUX DFFLA2 2021011T0000000_20500101T10000000_001_007_3 NO AUX DFFRA SM. OPER AUX DFFLA2 2021011T0000000_20500101T1000000_001_001_3 NO AUX DFFRA SM. OPER AUX DFFLA2 2021011T0000000_20500101T1000000_001_007_3 NO AUX DFFRA SM. OPER AUX DFFLA2 202102101T0000000_20500101T1000000_001_001_3 NO AUX DFFRA SM. DFFLA2 20210210000000_20500101T1000000_001_001_3 NO AUX DFFRA SM. DFFLA2 20210210000000_20500101T1000000_001_001_3 NO AUX DFFRA SM. DFFLA2 20210210000000_20500101T000000_001_3 NO AUX DFFRA SM. DFFLA2 2021021000000000000000000000000000000	AUX_BSCAT_	SM_OPER_AUX_BSCAT20050101T000000_20500101T000000_300_003_3	
AUX CNFAR SM_OPER_AUX CNFFAR_20050101T000000_20500101T000000_001_007_3.EEF NO AUX CNFLOP SM_OPER_AUX CNFLOP_20050101T000000_20500101T000000_001_007_3.EEF NO AUX CNFLOP SM_OPER_AUX CNFLOP_201050101T000000_20500101T000000_001_007_3.EEF NO AUX CNFNRT SM_OPER_AUX CNFLOP_201050101T000000_20500101T000000_002_001_23.EEF NO AUX CNFNRT SM_OPER_AUX CNFNRT_20050101T0000000_20500101T000000_002_001_23.EEF NO AUX CNFOSD SM_OPER_AUX CNFOSD_20050101T0000000_20500101T000000_001_002_3.EEF NO AUX CNFOSD SM_OPER_AUX CNFOSD_20050101T0000000_20500101T000000_001_003_3.EEF NO AUX CNFSMD SM_OPER_AUX CNFSMD_20050101T0000000_20500101T000000_001_017_3.EEF NO AUX CNFSMD SM_OPER_AUX CNFSMD_20050101T0000000_20500101T000000_001_017_3.EEF NO AUX CNFSMF SM_OPER_AUX CNFSMF_20050101T0000000_20500101T000000_001_017_3.EEF NO AUX CNFSMF SM_OPER_AUX DFFFLAL_20210101T0000000_20500101T000000_001_017_3.EEF NO AUX CNFSMF SM_OPER_AUX DFFLAL_20210101T0000000_20500101T000000_001_017_3.EEF NO AUX CNFSMF SM_OPER_AUX DFFLAL_20210101T0000000_20500101T000000_001_01_07_3.EEF NO AUX CNFSMF SM_OPER_AUX DFFLAL_20210101T0000000_20500101T000000_001_01_01_3 SM_OPER_AUX DFFLAL_20210101T0000000_2021021021T014000.600_001_3 SM_OPER_AUX DFFLAL_20210101T0000000_2021021021T014000.600_001_3 SM_OPER_AUX DFFLAL_2021031000000_2021031021014000.600_001_3 SM_OPER_AUX DFFLAL_202104021000000_202104027014000.600_001_3 SM_OPER_AUX DFFLAL_202104021000000_202104027014000.600_001_3 SM_OPER_AUX DFFLAL_202106101000000_202106210104000.600_001_3 SM_OPER_AUX DFFLAL_202106101000000_202106210104000.600_001_3 SM_OPER_AUX DFFLAL_202106100000000000000000000000000000000	AUX_BULL_B	SM_OPER_AUX_BULL_B_20210602T000000_20500101T000000_120_001_3	
AUX CNFLOP SM OPER AUX CNFLOP 20050101T000000 20500101T000000 001 007 3.EEF NO AUX CNFL1P SM OPER AUX CNFL1P 20110206T010100 20500101T000000 721 055 3.EEF NO AUX CNFNRT SM OPER AUX CNFNRT 20050101T000000 20500101T000000 001 029 3.EEF NO AUX CNFOSD SM OPER AUX CNFOSD 20050101T000000 20500101T000000 001 029 3.EEF NO AUX CNFOSD SM OPER AUX CNFOSD 20050101T000000 20500101T000000 001 023 3.EEF NO AUX CNFOSD SM OPER AUX CNFOSD 20050101T000000 20500101T000000 001 003 3.EEF NO AUX CNFSMD SM OPER AUX CNFSMD 20050101T000000 20500101T000000 001 007 3.EEF NO AUX CNFSMD SM OPER AUX CNFSMD 20050101T000000 20500101T000000 001 007 3.EEF NO AUX CNFSMD SM OPER AUX CNFSMF 20050101T000000 20500101T000000 001 007 3.EEF NO AUX CNFSMD SM OPER AUX DFFLA2 20210101T000000 20500101T000000 001 007 3.EEF NO AUX DFFLA2 20210101T000000 20500101T000000 001 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210101T000000 2021011T014000 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210101T000000 20210121T014000 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210121T000000 20210123T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210121T000000 2010123T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210121000000 201032T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210422T000000 201032T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 20210422T000000 201032T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 202104217000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 2021051T000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 2021051T000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 2021051T000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 2021051T000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 2021051T000000 201051T01400 600 001 3.EEF NO ENTRY SM OPER AUX DFFLA2 202105	AUX_BWGHT_	SM_OPER_AUX_BWGHT20050101T000000_20500101T000000_340_006_3.EEF	No
AUX_CNF1P	AUX_CNFFAR	SM_OPER_AUX_CNFFAR_20050101T000000_20500101T000000_100_002_3.EEF	No
AUX_CNFNRT MOPER_AUX_CNFORD_20050101T000000_20500101T000000_012_3.EEF NO AUX_CNFOSD SM_OPER_AUX_CNFOSD_20050101T000000_20500101T000000_01_029_3.EEF NO AUX_CNFOSF SM_OPER_AUX_CNFOSD_20050101T000000_20500101T000000_01_022_3.EEF NO AUX_CNFSMD SM_OPER_AUX_CNFSMD_20050101T000000_20500101T000000_01_017_3.EEF NO AUX_CNFSMF SM_OPER_AUX_CNFSMP_20050101T000000_20500101T000000_01_017_3.EEF NO AUX_CNFSMF SM_OPER_AUX_CNFSMP_20050101T000000_20500101T000000_01_017_3.EEF NO AUX_CNFSMF SM_OPER_AUX_DFFLAN_2005101T000000_20500101T000000_01_017_3.EEF NO AUX_CNFSMF SM_OPER_AUX_DFFLAN_2005101T000000_20500101T000000_01_017_3.EEF NO AUX_CNFSMF SM_OPER_AUX_DFFLAN_2005101T000000_20500101T000000_01_007_3 NO OPER_AUX_DFFLAN_20210101T000000_20210121T0140000_600_001_3 SM_OPER_AUX_DFFLAN_20210121T000000_20210121T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210121T000000_20210123T014000_600_001_3 SM_OPER_AUX_DFFLAN_2021023T000000_20210123T014000_600_001_3 SM_OPER_AUX_DFFLAN_2021032T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAN_2021032T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210422T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210422T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAN_2021042T0100000_202101801T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_2021061T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_2021061T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_2021061T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_2021061T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAN_20210601T000000_202	AUX_CNFL0P	SM_OPER_AUX_CNFL0P_20050101T000000_20500101T000000_001_007_3.EEF	No
AUX_CNFOSD	AUX_CNFL1P	SM_OPER_AUX_CNFL1P_20110206T010100_20500101T000000_721_055_3.EEF	No
AUX_CNFOSF	AUX_CNFNRT	SM_OPER_AUX_CNFNRT_20050101T000000_20500101T000000_620_012_3.EEF	No
AUX_CNFSMD	AUX_CNFOSD	SM_OPER_AUX_CNFOSD_20050101T000000_20500101T000000_001_029_3.EEF	No
AUX_CNFSMF	AUX_CNFOSF	SM_OPER_AUX_CNFOSF_20050101T000000_20500101T000000_001_032_3.EEF	No
AUX_DFFFRA SM_OPER_AUX_DFFFRA_20050101T000000_20500101T000000_001_007_3 SM_OPER_AUX_DFFLAL_20210101T000000_20210111T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210111T000000_20210121T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210121T000000_20210123T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210213T000000_2021023T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210213T000000_2021023T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210233T000000_20210323T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210322T000000_20210322T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210322T000000_20210322T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210322T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210402T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210402T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210422T000000_20210501T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021051T000000_20210521T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021051T000000_20210521T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021051T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210621T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210601T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210601T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210601T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210621T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210631T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210631T000000_20210631T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210631T000000_20210631T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210630T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210809T000000_2021089T014000_600_001_3 SM_OPER_AUX_DFFLAL_20210809T000000_2021089T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021089T000000_2021089T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021089T000000_2021089T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021108T000000_2021108T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021108T000000_2021108T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021108T000000_2021108T014000_600_001_3 SM_OPER_AUX_DFFLAL_2021108T000000_2021108T014000_600_001_3 SM_OPER	AUX_CNFSMD	SM_OPER_AUX_CNFSMD_20050101T000000_20500101T000000_001_017_3.EEF	No
SM_OPER_AUX_DFFLAI_20210110100000_202101111T014000_600_001_3 SM_OPER_AUX_DFFLAI_202101111T000000_20210121T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021012117000000_20210123T014000_600_001_3 SM_OPER_AUX_DFFLAI_202102307000000_20210230714000_600_001_3 SM_OPER_AUX_DFFLAI_202102307000000_202102237014000_600_001_3 SM_OPER_AUX_DFFLAI_202102307000000_20210237014000_600_001_3 SM_OPER_AUX_DFFLAI_2021032071000000_20210327014000_600_001_3 SM_OPER_AUX_DFFLAI_2021032071000000_20210327014000_600_001_3 SM_OPER_AUX_DFFLAI_202103217000000_20210327014000_600_001_3 SM_OPER_AUX_DFFLAI_202103217000000_202104227014000_600_001_3 SM_OPER_AUX_DFFLAI_202104027000000_202104227014000_600_001_3 SM_OPER_AUX_DFFLAI_202104027000000_202104227014000_600_001_3 SM_OPER_AUX_DFFLAI_202104217000000_202105117014000_600_001_3 SM_OPER_AUX_DFFLAI_2021050171000000_202105117014000_600_001_3 SM_OPER_AUX_DFFLAI_2021050171000000_202106117014000_600_001_3 SM_OPER_AUX_DFFLAI_2021050171000000_202106117014000_600_001_3 SM_OPER_AUX_DFFLAI_2021050171000000_202106117014000_600_001_3 SM_OPER_AUX_DFFLAI_202105017000000_202106317014000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_202106317014000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_202106317014000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_202106317014000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_2021072070140000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_2021072070140000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_2021072070140000_600_001_3 SM_OPER_AUX_DFFLAI_202106017000000_20210980104000_600_001_3 SM_OPER_AUX_DFFLAI_2021080917000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_2021080917000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_2021080917000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_2021080917000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_2021080917000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_202110807000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_202110807000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_2021108091000000_202108091704000_600_001_3 SM_OPER_AUX_DFFLAI_202	AUX_CNFSMF	SM_OPER_AUX_CNFSMF_20050101T000000_20500101T000000_001_017_3.EEF	No
SM_OPER_AUX_DFFLAI_20210111T000000_20210121T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210121T000000_20210203T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210203T000000_20210223T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021023T000000_2021023T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_2021032T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210412T000000_20210422T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021042T000000_20210422T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021042T000000_20210501T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021042T000000_20210501T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021051T000000_20210501T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021051T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021051T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021051T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210631T000000_20210621T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210630T000000_20210720T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210630T000000_20210720T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210630T000000_20210720T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210720T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210830T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210819T000000_20210829T014000_600_001_3 SM_OPER_AUX_DFFLAI_20211080T000000_202110280T04000_600_001_3 SM_OPER_AUX_DFFLAI_20211080T000000_202110280T04000_600_001_3 SM_OPER_AUX_DFFLAI_202111080T000000_202110280T04000_600_001_3 SM_OPER_AUX_DFFLAI_202111080T000000_20211	AUX_DFFFRA	SM_OPER_AUX_DFFFRA_20050101T000000_20500101T000000_001_007_3	No
AUX_DFFLMX	AUX_DFFLAI	SM_OPER_AUX_DFFLAI_20210121T000000_20210203T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021023T000000_20210223T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210233T000000_20210223T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210233T000000_20210302T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210302T000000_20210312T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210312T000000_20210312T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210322T000000_202104022T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210402T000000_20210402T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210402T000000_20210412T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210422T000000_20210422T014000_600_001_3 SM_OPER_AUX_DFFLAI_202104022T000000_20210521T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210501T000000_20210521T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210511T000000_20210521T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210521T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210611T000000_20210601T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210611T000000_20210630T014000_600_001_3 SM_OPER_AUX_DFFLAI_2021061T000000_20210630T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210630T000000_20210630T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210630T000000_20210630T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_20210730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110730T000000_20210730T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110809T000000_202110809T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110809T000000_202110819T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110809T000000_202110819T014000_600_001_3 SM_OPER_AUX_DFFLAI_202110817000000_202110817014000_600_001_3 SM_OPER_AUX_DFFLAI_20211108T000000_20211108T014000_600_001_3 SM_OPER_AUX_DFFLAI_20211118T0000000_20211108T014000_600_001_3 SM_OPER_AUX_DFFLAI_20211	No
No.	ALLY DEELAY		No
AUX_DFF301 3M_OPEK_AUX_DFF301_Z00301011000000_Z050001011000000_001_003_3 140			
AUX_DFFXYZ			

AUX DGGYYZ SM OPER AUX DGGYYZ 20050101T000000 20500101T000000 001 001 3 No AUX DISTAN SM OPER AUX DISTAN 20050101T000000 2050010T000000 001 011 3 No AUX DTBCUR SM REPR AUX DTBCUR 20210520T083700 2050010T000000 699 320_1 Initialization file for the deployment of the L205 V700 processor AUX ECOLAI SM OPER AUX ECOLAI 20050101T000000 2050010T000000 699 320_1 AUX ECOLAI SM OPER AUX ECOLAI 2005010T1000000 2050010T000000 01 003 3 SEFF No AUX ECMCDF SM OPER AUX ECMCDF 2005010T1000000 2050010T000000 001 003 3 AUX FAILS SM OPER AUX FAIL 2005010T1000000 2050010T1000000 001 001 3 SEFF No AUX FAILS SM OPER AUX FAIL 2005010T1000000 2050010T1000000 001 001 3 No AUX FAISHEL SM OPER AUX FAIL 2005010T1000000 2050010T1000000 001 001 3 No AUX FAISHEL SM OPER AUX FOAM 2005010T1000000 2050010T1000000 001 001 3 No AUX GAL OS SM OPER AUX FOAM 2005010T1000000 2050010T1000000 001 001 3 No AUX GAL OS SM OPER AUX FOAM 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GAL MS 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GAL MS 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GAL SM 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GAL SM 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GAL SM 2005010T1000000 2050010T1000000 01 001 3 No AUX GALOS SM OPER AUX GALNAY 2005010T1000000 2050010T1000000 001 001 3 No AUX GALNE SM OPER AUX GALNAY 2005010T1000000 2050010T1000000 001 005 3 SEFF No AUX GALNE SM OPER AUX CALNA 2005010T1000000 2050010T1000000 001 005 3 SEFF No AUX GALNE SM OPER AUX CALNA 2005010T1000000 2050010T1000000 200 003 000 3 No AUX LEMASK SM OPER AUX CALNA 2005010T1000000 2050010T1000000 200 003 000 3 No AUX LEMASK SM OPER AUX MN WEF 2005010T1000000 2050010T1000000 200 000 000 000 000 000 000 000			1
AUX_DISTAN SM_OPER_AUX_DISCUS 2021652071030000_20500101T000000_011_13 NO AUX_DTBCUR SM_REPR_AUX_DTBCUR_2021652071033700_20500101T000000_699_320_1 Initialization file for the deployment of the L2OS V700 processor. AUX_ECOLAI SM_OPER_AUX_ECOLD_202161071000000_20500101T000000_01_003_3EFF NO AUX_ECMCDF SM_OPER_AUX_ECMCDF_201011097000000_20500101T000000_01_003_3EFF NO AUX_ECMCDF SM_OPER_AUX_ECMCDF_201011097000000_20500101T000000_01_003_3EFF NO AUX_FLTSEA SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_01_003_3EFF NO AUX_FLTSEA SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_001_003_3EFF NO AUX_FLTSEA SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_001_003_3EFF NO AUX_FLTSEA SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_001_011_3 NO AUX_FOAM SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_01_011_3 NO AUX_GAL_SM SM_OPER_AUX_ECMCDF_20050101T000000_20500101T000000_01_011_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_SM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_SM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_XM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_XM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_XM_20050101T000000_20500101T000000_30_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_XM_20050101T000000_20500101T000000_30_003_3 NO AUX_LCF SM_OPER_AUX_LCF 20050101T000000_20500101T000000_30_003_3 NO AUX_LCF SM_OPER_AUX_LCF 20050101T000000_20500101T000000_30_003_3 NO AUX_MSSS SM_OPER_AUX_MSSS SM_OPER_AUX_MSSSS SM_OPER_AUX_MSSSSS SM_OPER_AUX_MSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSS	AUX_DGG	SM_OPER_AUX_DGG20050101T000000_20500101T000000_300_003_3	No
AUX DTECUR SM_REPR_AUX_DTECUR_20210520T083700_20500101T000000_689_320_1 Initialization file for the deployment of the L2CS Y700 processor. AUX_ECOLAI SM_OPER_AUX_ECOLA_D005010T1000000_2050010T1000000_01_003_3.EEF NO SM_OPER_AUX_ECOLA_D005010T1000000_2050010T1000000_01_003_3.EEF NO AUX_FAIL SM_OPER_AUX_ECOLA_D005010T1000000_2050010T1000000_01_003_3.EEF NO AUX_FAIL SM_OPER_AUX_ECOLA_D005010T1000000_2050010T1000000_001_01_3.EEF NO AUX_FAIL SM_OPER_AUX_FAIL _2005010T1000000_2050010T1000000_001_01_2.3.EEF NO AUX_FAIL SM_OPER_AUX_FAIL _2005010T1000000_2050010T1000000_001_01_3.EEF NO AUX_FAIL SM_OPER_AUX_FROM_2005010T1000000_2050010T1000000_01_13.EEF NO AUX_FAIL SM_OPER_AUX_FROM_2005010T1000000_2050010T1000000_01_13.EEF NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_2005010T1000000_2050010T1000000_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_2005010T1000000_2050010T1000000_01_01_13.NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_2005010T1000000_2050010T1000000_01_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_2005010T1000000_2050010T1000000_01_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_2005010T1000000_2050010T1000000_01_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_NOS_2005010T1000000_2050010T1000000_01_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_NOS_2005010T1000000_2050010T1000000_01_01_01_3.NO AUX_GAL_OS SM_OPER_AUX_GAL_NOS_2005010T1000000_2050010T1000000_001_01_01_3.NO AUX_GAL_NOS SM_OPER_AUX_GAL_NOS_2005010T1000000_2050010T1000000_001_01_01_3.NO AUX_LSG_SM_OPER_AUX_GAL_NOS_2005010T1000000_2050010T1000000_001_003_3.REF NO AUX_LSG_SM_OPER_AUX_LSC_2005010T1000000_2050010T1000000_001_003_3.EEF NO AUX_LSM_SK DSC_DER_AUX_MSS_2005010T1000000_2050010T1000000_001_003_3.NO AUX_MSS_SM_OPER_AUX_MSS_2005010T1000000_2050010T1000000_000_000_000_000_000_000_000	AUX_DGGXYZ	SM_OPER_AUX_DGGXYZ_20050101T000000_20500101T000000_001_004_3	
AUX_COTT2D_ AUX_CO	AUX_DISTAN	SM_OPER_AUX_DISTAN_20050101T000000_20500101T000000_001_011_3	
AUX ECMODF SM. OPER AUX EMODE 200501017000000 20100000 001 003 3 EFF NO AUX FAIL SM. OPER AUX FAIL 200501017000000 201017000000 001 012 3 EEF NO AUX FLTSEA SM. OPER AUX FAIL 200501017000000 205001017000000 01 012 3 EEF NO AUX FLTSEA SM. OPER AUX FAIL 200501017000000 205001017000000 01 012 3 EEF NO AUX FRINEL SM. OPER AUX FAIL 200501017000000 205001017000000 01 011 3 NO AUX FRSNEL SM. OPER AUX FAIL 200501017000000 205001017000000 01 011 3 NO AUX FRSNEL SM. OPER AUX FAIL 200501017000000 205001017000000 01 011 3 NO AUX GAL OS SM. OPER AUX GAL OS 200501017000000 205001017000000 01 013 NO AUX GAL OS SM. OPER AUX GAL SM 200501017000000 205001017000000 01 003 3 NO AUX GALAYS SM. OPER AUX GALEOS 200501017000000 205001017000000 01 003 NO AUX GALAYS SM. OPER AUX GALAXY 200501017000000 205001017000000 01 003 NO AUX GALAYS SM. OPER AUX GALAXY 200501017000000 205001017000000 300 004 NO AUX GALAYS SM. OPER AUX GALAXY 200501017000000 205001017000000 300 003 NO AUX LANDCL SM. OPER AUX LANDCL 200501017000000 205001017000000 300 003 SEFF NO AUX LANDCL SM. OPER AUX LANDCL 200501017000000 205001017000000 200 30 NO AUX LANDS M. OPER AUX LANDCL 200501017000000 205001017000000 300 002 NO AUX LANDS SM. OPER AUX LANDCL 200501017000000 20500101700000 300 002 NO AUX LANDS SM. OPER AUX LANDCL 200501017000000 205001017000000 300 002 NO AUX MASK SM. OPER AUX MASK 200501017000000 205001017000000 300 002 NO AUX MISP SM. OPER AUX MASK 200501017000000 205001017000000 300 002 NO AUX MISP SM. OPER AUX MONT 200501017000000 205001017000000 300 002 NO AUX MOONT SM. OPER AUX MOONT 200501017000000 205001017000000 300 004 3EFF NO AUX MOONT SM. OPER AUX NOTTE 200501017000000 205001017000000 300 004 3EFF NO AUX NOTT1D SM. OPER AUX NOTTE 200501017000000 205001017000000 300 004 3EFF NO AUX NOTTSD SM. OPER AUX NOTTE 200501017000000 205001017000000 300 004 3EFF NO AUX NOTTSD SM. SEPR AUX NOTTSD 200501017000000 205001017000000 300 004 3EFF NO AUX NOTTSD SM. REPR AUX NOTTSD 20010500000 205001017000000	AUX_DTBCUR		No
AUX ECMCDF MOPER AUX EMIL SM, OPER AUX FAIL 200501017000000 25001017000000 001_003.3 MOX FILTSEA MOPER AUX FILE 200501017000000 25001017000000 001_012_3.EEF NO AUX FISEA MOPER AUX FILESA 200501017000000 25001017000000 001_011_3 NO AUX FRSNEL MOPER AUX FRSNEL 200501017000000 25001017000000 001_011_3 NO AUX GAL SS MOPER AUX GAL SW 200501017000000 25001017000000 001_011_3 NO AUX GAL SS MOPER AUX GAL SW 200501017000000 25001017000000 001_010_13 NO AUX GAL SS MOPER AUX GAL SW 200501017000000 25001017000000 001_010_3 NO AUX GAL SS MOPER AUX GAL SW 200501017000000 25001017000000 001_010_3 NO AUX GAL SS MOPER AUX GAL SW 200501017000000 25001017000000 001_016_3 NO AUX GALNO SM_OPER AUX GALNO	AUX_ECOLAI	SM_OPER_AUX_ECOLAI_20050101T000000_20500101T000000_305_006_3	No
AUX_FLTSEA SM_OPER_AUX_FLTSEA_20050101T000000_20500101T000000_001_012_3 EEF NO AUX_FRANK SM_OPER_AUX_FRANKL_20050101T000000_20500101T000000_01_011_3 NO AUX_FRSNEL SM_OPER_AUX_FRSNEL_20050101T000000_20500101T000000_01_011_3 NO AUX_GAL_OS SM_OPER_AUX_GAL_OS_20050101T000000_20500101T000000_01_011_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_SM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_SM_20050101T000000_20500101T000000_01_003_3 NO AUX_GAL_SM SM_OPER_AUX_GAL_SM_20050101T000000_20500101T000000_01_003_3 NO AUX_GALNY SM_OPER_AUX_GALNINC_20050101T000000_20500101T000000_300_004_3 NO AUX_GALNY SM_OPER_AUX_GALNINC_20050101T000000_20500101T000000_300_004_3 NO AUX_LANDCL SM_OPER_AUX_GALNINC_20050101T000000_20500101T000000_300_003_3 NO AUX_LSMASK SM_OPER_AUX_GALNINC_20050101T000000_20500101T000000_300_003_3 NO AUX_LSMASK Discontinued (dynamic ADF for V7xx baseline onwards) NO AUX_MSK_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSK_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSK_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSP_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSP_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSP_ SM_OPER_AUX_MSK_20050101T000000_20500101T000000_300_002_3 NO AUX_MSOTT_ SM_OPER_AUX_MSOTT_20050101T000000_20500101T000000_300_002_3 NO AUX_NSTMSK SM_OPER_AUX_NSTMSK_20050101T000000_20500101T000000_300_002_3 NO AUX_NSTMSK SM_OPER_AUX_NSTMSK_20050101T000000_20500101T000000_207_001_3_EFF NO AUX_NSTMSK SM_OPER_AUX_NSTMSK_20050101T000000_20500101T000000_207_001_6 NO AUX_NSTMSK SM_OPER_AUX_NTTSM_2005101T000000_20500101T000000_200_300_012_3_EFF NO AUX_OTT1D_ Initialization lile for the deployment of the L2OS V700 processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor SM_REPR_AUX_OTT3D_20210520T083700_20500101T000000_699_320_1 Initialization lile for the deploym	AUX_ECMCDF	SM_OPER_AUX_ECMCDF_20101109T000000_20500101T000000_001_003_3.EEF	No
AUX FOAM	AUX_FAIL	SM_OPER_AUX_FAIL20050101T000000_20500101T000000_300_004_3.EEF	No
AUX FRSNEL M. OPER, AUX FRSNEL 20050101T000000, 20500101T000000, 720, 001, 3 NO AUX GAL, SM SM, OPER, AUX GAL, OS, 20050101T000000, 20500101T000000, 001, 011, 3 NO AUX GAL, SM SM, OPER, AUX GAL, SM, 20050101T000000, 20500101T000000, 001, 011, 3 NO AUX GALASY SM, OPER, AUX GAL, SM, 20050101T000000, 20500101T000000, 001, 016, 3 NO AUX GALAXY SM, OPER, AUX, GALAYY, 20050101T000000, 20500101T000000, 300, 004, 3 NO AUX GALAXY SM, OPER, AUX, GALAYY, 20050101T000000, 20500101T000000, 300, 003, 3 NO AUX GALNIR SM, OPER, AUX, GALNIR, 20050101T000000, 20500101T000000, 300, 003, 3 NO AUX LOF SM, OPER, AUX, LANDCL, 20050101T000000, 20500101T000000, 300, 003, 3 AUX LOF SM, OPER, AUX, LANDCL, 20050101T000000, 20500101T000000, 300, 003, 3 AUX LOF SM, OPER, AUX, MSK, 20050101T000000, 20500101T000000, 200, 001, 3 AUX MSK SM, OPER, AUX, MSK, 20050101T000000, 20500101T000000, 300, 002, 3 NO AUX, MISSP SM, OPER, AUX, MSK, 20050101T000000, 20500101T000000, 300, 002, 3 NO AUX, MISSP SM, OPER, AUX, MSK, 20050101T000000, 20500101T000000, 300, 002, 3 NO AUX, MISSP SM, OPER, AUX, MISSP SM, OPER, AUX, MISSP SM, OPER, AUX, MISSP SM, OPER, AUX, MISSP AUX, NESSP SM, OPER, AUX, MISSP AUX, NESSP SM, OPER, AUX, NISSP SM, OPER, AUX, NISSP AUX, OTTID Initialization file for the deployment of the L2OS V700 processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor. Since level 2 OS processor V62x the new file is generated o	AUX_FLTSEA	SM_OPER_AUX_FLTSEA_20050101T000000_20500101T000000_001_012_3.EEF	No
AUX GAL_OS	AUX_FOAM	SM_OPER_AUX_FOAM20050101T000000_20500101T000000_001_011_3	No
AUX GAL SM SM OPER AUX GAL SM 20050101T000000 20500101T000000 001_0016_3 NO AUX GAL2OS SM_OPER_AUX_GAL2OS_20050101T000000_20500101T000000_001_016_3 NO AUX GALAXY SM_OPER_AUX_GAL2OS_20050101T000000_20500101T000000_300_003_3 NO AUX GALANIR SM_OPER_AUX_GALAXY_20050101T000000_20500101T000000_300_003_3 NO AUX LANDCL SM_OPER_AUX_GALAXY_20050101T000000_20500101T000000_300_003_3 NO AUX_LANDCL SM_OPER_AUX_LOF20050101T000000_20500101T000000_720_019_3.EEF NO AUX_LANDCL SM_OPER_AUX_LOF20050101T000000_20500101T000000_720_019_3.EEF NO AUX_LANDCL SM_OPER_AUX_LOF20050101T000000_20500101T000000_720_019_3.EEF NO AUX_LANDCL SM_OPER_AUX_MASK20050101T000000_20500101T000000_720_019_3.EEF NO AUX_MSSK	AUX_FRSNEL	SM_OPER_AUX_FRSNEL_20050101T000000_20500101T000000_720_001_3	No
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AUX_LCF		SM_OPER_AUX_LANDCL_20050101T000000 20500101T000000 001 006 3.EEF	No
AUX_LSMASK Discontinued (dynamic ADF for V7xx baseline onwards) No			No
AUX_MSK		Discontinued (dynamic ADF for V7xx baseline onwards)	No
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AUX_OTT3F_ AUX_OTT3F_ AUX_OTT3F_ Initialization file for the deployment of the L2OS V700 processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2 post processor AUX_PATT_ SM_OPER_AUX_PATT20050101T000000_20500101T000000_720_004_3 NO	AUX_OTT3D_	SM_REPR_AUX_OTT3D20210520T083700_20500101T000000_699_320_1 Initialization file for the deployment of the L2OS V700 processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2	No
AUX_PATT	AUX_OTT3F_	Initialization file for the deployment of the L2OS V700 processor. Since level 2 OS processor V62x the new file is generated on routine basis by the level 2	No
ON OPER ANY RIVE CONTROL OF CONTR	AUX_PATT		No
	AUX_PLM	SM_OPER_AUX_PLM20050101T000000_20500101T000000_600_008_3.EEF	No



AUX_PMS	SM_OPER_AUX_PMS20050101T000000_20500101T000000_711_013_3.EEF	No
AUX_RFI	SM_OPER_AUX_RFI20050101T000000_20500101T000000_300_003_3	No
AUX_RFILST	Since level 1 processor version V62x the file is generated by CATDS on monthly basis	No
AUX_RGHNS1	SM_OPER_AUX_RGHNS1_20050101T000000_20500101T000000_001_016_3	No
AUX_RGHNS2	SM_OPER_AUX_RGHNS2_20050101T000000_20500101T000000_001_013_3	No
AUX_RGHNS3	SM_OPER_AUX_RGHNS3_20050101T000000_20500101T000000_001_016_3	No
AUX_SGLINT	SM_OPER_AUX_SGLINT_20050101T000000_20500101T000000_001_012_3	No
AUX_SOIL_P	Discontinued (replaced by AUX_DFFSOI for V6xx baseline onwards)	No
AUX_SPAR	SM_OPER_AUX_SPAR20110112T091500_20500101T000000_720_013_3.EEF SM_OPER_AUX_SPAR20100111T120700_20110112T091500_720_013_3.EEF SM_OPER_AUX_SPAR20050101T000000_20100111T120700_720_013_3.EEF	No
AUX_SSS	SM_OPER_AUX_SSS20050101T000000_20500101T000000_001_014_3	No
AUX_SSSCLI	SM_OPER_AUX_SSSCLI_20050101T000000_20500101T000000_001_002_3	No
AUX_SUNT	SM_OPER_AUX_SUNT20050101T000000_20500101T000000_300_002_3	No
AUX_WEF	SM_OPER_AUX_WEF20050101T000000_20500101T000000_001_003_3	No
MPL_ORBSCT	SM_OPER_MPL_ORBSCT_20091102T031142_20500101T000000_454_002_1.EEF	No

8 APPENDIX A. CONFIGURATION DOCUMENT LIST

The list of internal documents used for the generation of this report is:

- Unavailability.xls
- Details_Calibrations.xls
- SMOS-CEC-VEG-IPF-REP-0609_v2.08_SMOS_Auxiliary_Data_File_List_20210520_signed.pdf

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